

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> T <sub>A</sub> = +25°C
30V	23mΩ @ V <sub>GS</sub> = 10V	6.6A
	30mΩ @ V <sub>GS</sub> = 4.5V	5.8A

## Description

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>), yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

## Applications

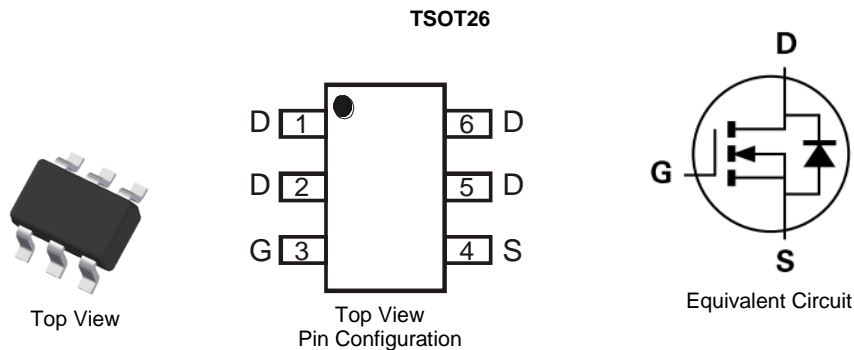
- DC-DC Converters
- Power management functions
- Backlighting

## Features and Benefits

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

## Mechanical Data

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.013 grams (Approximate)

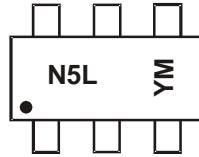


## Ordering Information (Note 5)

Part Number	Case	Packaging
DMN3026LVTQ-7	TSOT26	3,000/Tape & Reel
DMN3026LVTQ-13	TSOT26	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to [http://www.diodes.com/quality/product\\_compliance\\_definitions/](http://www.diodes.com/quality/product_compliance_definitions/).
  5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



N5L = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: A = 2013)  
 M = Month (ex: 9 = September)

### Date Code Key

Year	2010	...	2014	2015	2016	2017	2018	2019	2020	2021	2022
Code	X	...	B	C	D	E	F	G	H	I	J

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V <sub>DSS</sub>	30	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 7) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = +25°C	6.6
		T <sub>A</sub> = +70°C	5.3
Continuous Drain Current (Note 7) V <sub>GS</sub> = 10V	t < 10s	T <sub>A</sub> = +25°C	8.5
		T <sub>A</sub> = +70°C	6.8
Maximum Body Diode Forward Current (Note 7)	I <sub>S</sub>	3.0	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	35	A

## Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 6)	P <sub>D</sub>	T <sub>A</sub> = +25°C	1.2
		T <sub>A</sub> = +70°C	0.8
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	Steady state	100
		t < 10s	60
Total Power Dissipation (Note 7)	P <sub>D</sub>	T <sub>A</sub> = +25°C	1.5
		T <sub>A</sub> = +70°C	1.0
Thermal Resistance, Junction to Ambient (Note 7)	R <sub>θJA</sub>	Steady state	83
		t < 10s	50
Thermal Resistance, Junction to Case (Note 7)	R <sub>θJC</sub>	14.5	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1.0	μA	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0	1.5	2.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	19	23	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6.5A
		—	22	30		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 6.0A
Diode Forward Voltage	V <sub>SD</sub>	—	0.7	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1.0A
<b>DYNAMIC CHARACTERISTICS (Note 9)</b>						
Input Capacitance	C <sub>iss</sub>	—	643	—	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	65	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	49	—		
Gate Resistance	R <sub>G</sub>	—	2.5	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>g</sub>	—	5.7	—	nC	V <sub>DS</sub> = 15V, I <sub>D</sub> = 4.0A
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>g</sub>	—	12.5	—		
Gate-Source Charge	Q <sub>gs</sub>	—	1.7	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	1.8	—		
Turn-On Delay Time	t <sub>D(on)</sub>	—	2.2	—	nS	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 15V, R <sub>G</sub> = 6.0Ω, I <sub>D</sub> = 6.5A
Turn-On Rise Time	t <sub>r</sub>	—	2.5	—		
Turn-Off Delay Time	t <sub>D(off)</sub>	—	12.1	—		
Turn-Off Fall Time	t <sub>f</sub>	—	3.0	—		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	—	6.5	—	nS	I <sub>F</sub> = 6.5A, dI/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	—	1.7	—	nC	I <sub>F</sub> = 6.5A, dI/dt = 100A/μs

- Notes:
6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.
  8. Short duration pulse test used to minimize self-heating effect.
  9. Guaranteed by design. Not subject to product testing.

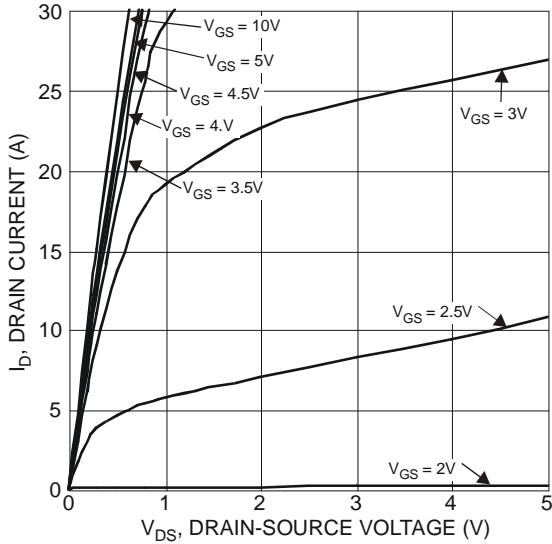


Figure 1 Typical Output Characteristics

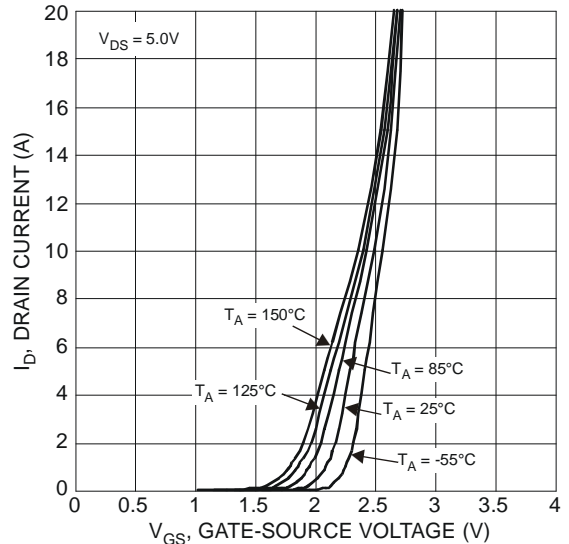


Figure 2 Typical Transfer Characteristics

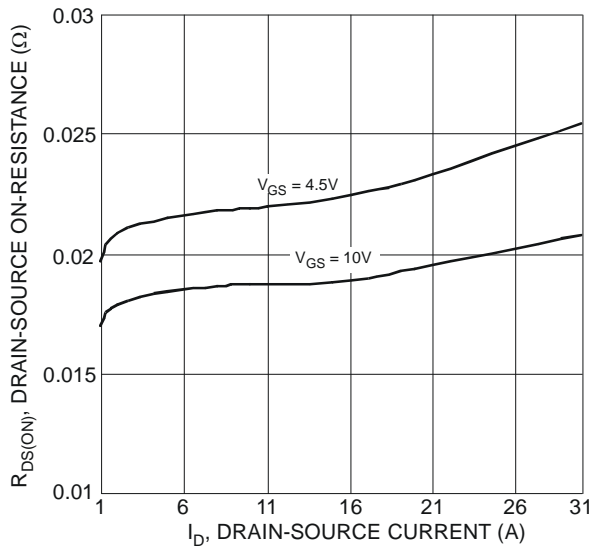


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

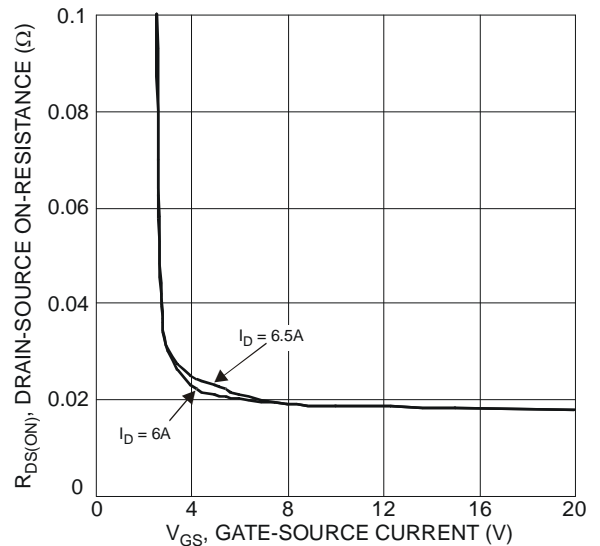


Figure 4 Typical Transfer Characteristics

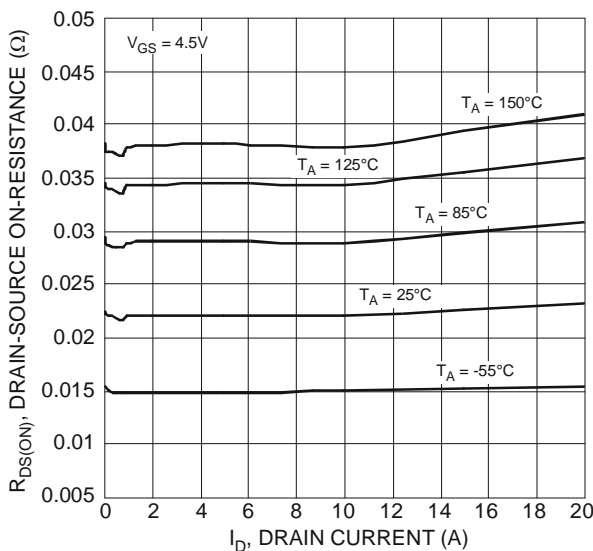


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

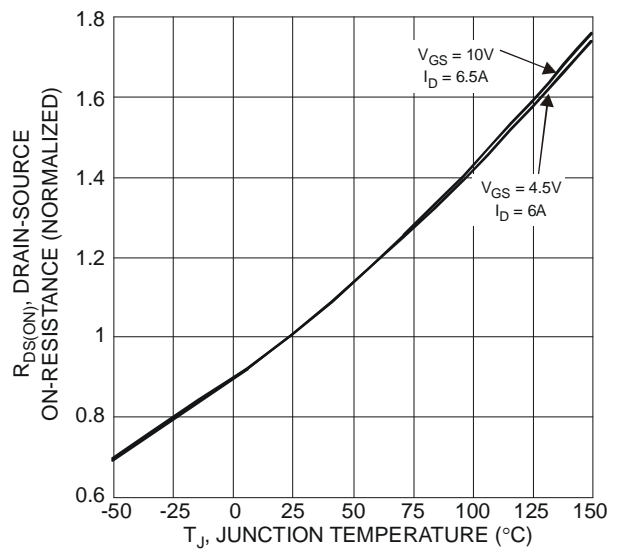


Figure 6 On-Resistance Variation with Temperature

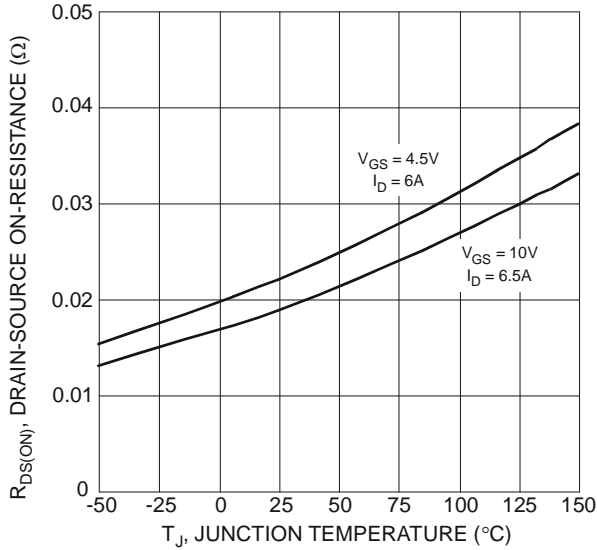


Figure 7 On-Resistance Variation with Temperature

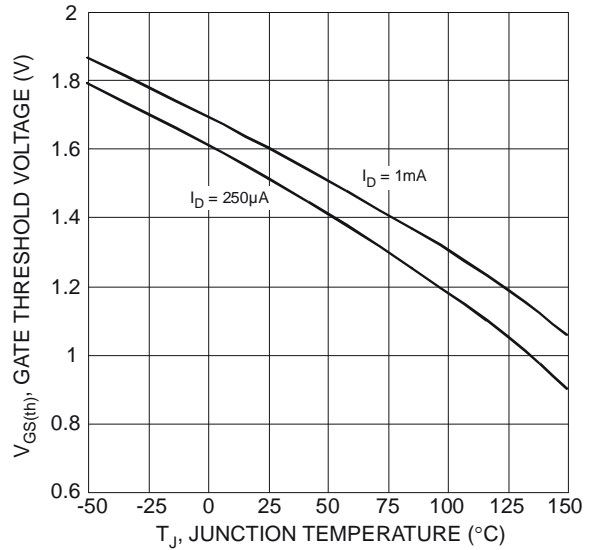


Figure 8 Gate Threshold Variation vs. Ambient Temperature

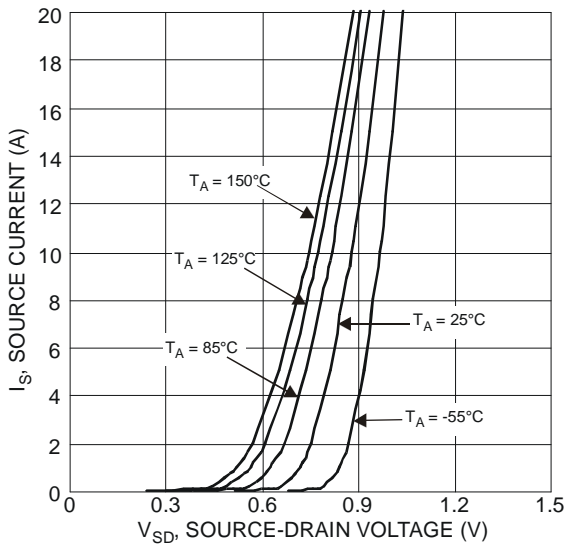


Figure 9 Diode Forward Voltage vs. Current

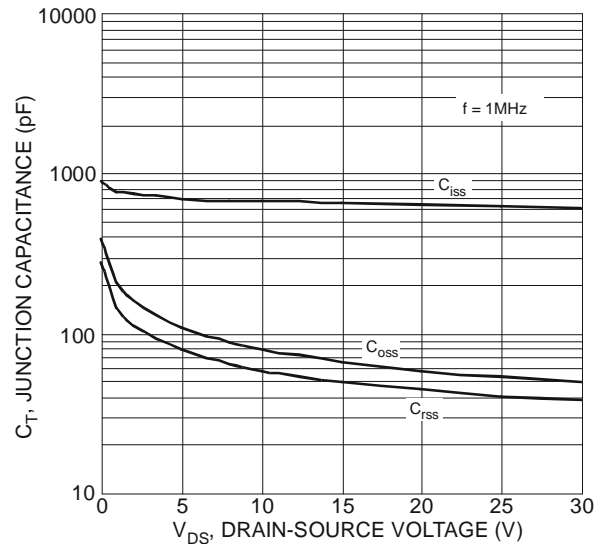


Figure 10 Typical Junction Capacitance

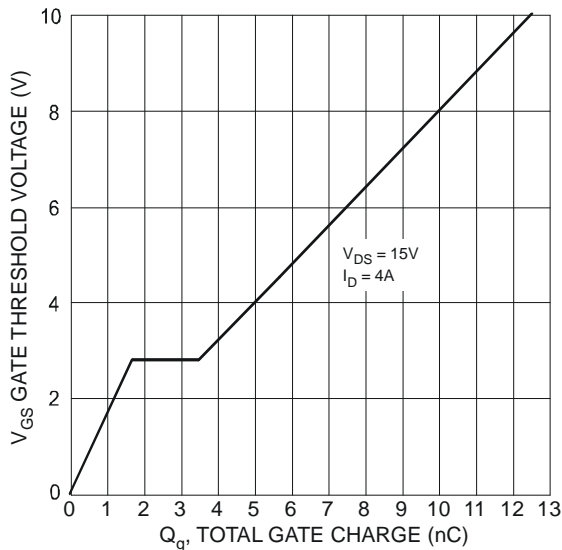


Figure 11 Gate Charge

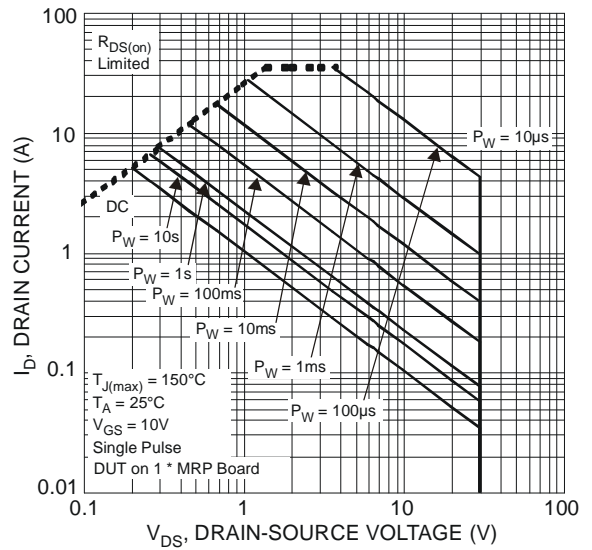
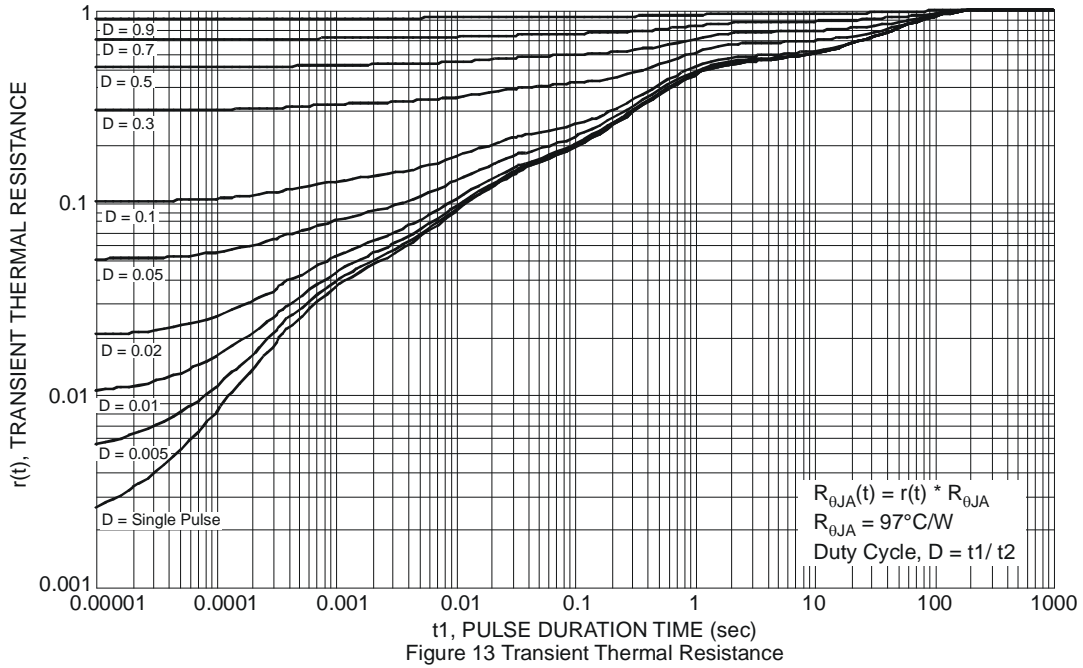


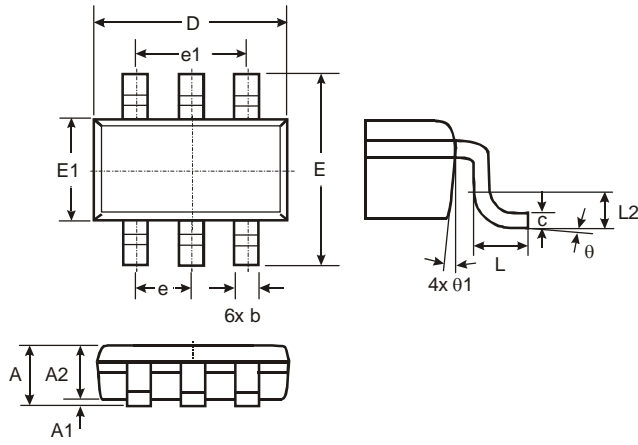
Figure 12 SOA, Safe Operation Area



**Package Outline Dimensions**

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

**TSOT26**

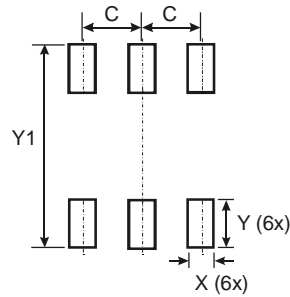


TSOT26			
Dim	Min	Max	Typ
A	—	1.00	—
A1	0.01	0.10	—
A2	0.84	0.90	—
D	—	—	2.90
E	—	—	2.80
E1	—	—	1.60
b	0.30	0.45	—
c	0.12	0.20	—
e	—	—	0.95
e1	—	—	1.90
L	0.30	0.50	—
L2	—	—	0.25
$\theta$	0°	8°	4°
$\theta1$	4°	12°	—
All Dimensions in mm			

**Suggested Pad Layout**

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.

**TSOT26**



Dimensions	Value (in mm)
C	0.950
X	0.700
Y	1.000
Y1	3.199

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