FAIRCHILD

SEMICONDUCTOR®

May 2010

FDB8832_F085

N-Channel Logic Level PowerTrench® MOSFET

30V, 80A, 2.1mΩ

Features

- Typ $r_{DS(on)}$ = 1.5mΩ at V_{GS} = 5V, I_D = 80A

- Typ $Q_{g(5)}$ = 100nC at V_{GS} = 5V

- Low Miller Charge

- Low Q_{rr} Body Diode

- UIS Capability (Single Pulse and Repetitive Pulse)

- Qualified to AEC Q101

- RoHS Compliant

Applications

- 12V Automotive Load Control

- Starter / Alternator Systems

- Electronic Power Steering Systems

- ABS

- DC-DC Converters

GATE

SOURCE

DRAIN
(FLANGE)

TO-263AB
FDB SERIES

D

G

S

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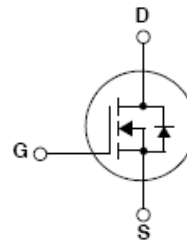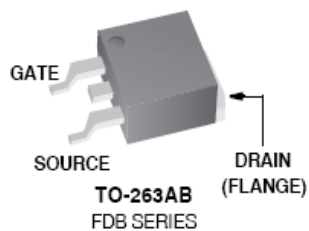
MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V_{DSS}	Drain to Source Voltage		30	V
V_{GS}	Gate to Source Voltage		±20	V
I_D	Drain Current Continuous (T_C < 165°C, V_{GS} = 10V)		80	A
	Drain Current Continuous (T_C < 163°C, V_{GS} = 5V)		80	
	Drain Current Continuous (T_{amb} = 25°C, V_{GS} = 10V, with $R_{\theta JA}$ = 43°C/W)		34	
	Pulsed		See Figure 4	
E_{AS}	Single Pulse Avalanche Energy	(Note 1)	1246	mJ
P_D	Power Dissipation		300	W
	Derate above 25°C		2	W/°C
T_J, T_{STG}	Operating and Storage Temperature		-55 to +175	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case		0.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 2)	62	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, lin^2 copper pad area		43	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDB8832	FDB8832_F085	TO-263AB	330mm	24mm	800 units

Electrical Characteristics T_J = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions		Min	Typ	Max	Units
Off Characteristics							
B_{VDSS}	Drain to Source Breakdown Voltage	I_D = 250µA, V_{GS} = 0V		30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 24V V_{GS} = 0V		-	-	1	µA
			T_J = 150°C	-	-	250	
I_{GSS}	Gate to Source Leakage Current	V_{GS} = ±20V		-	-	±100	nA
On Characteristics							
$V_{GS(th)}$	Gate to Source Threshold Voltage	V_{DS} = V_{GS}, I_D = 250µA		1.0	1.6	3.0	V
$r_{DS(on)}$	Drain to Source On Resistance	I_D = 80A, V_{GS} = 10V		-	1.4	1.9	mΩ
		I_D = 80A, V_{GS} = 5V		-	1.5	2.1	
		I_D = 80A, V_{GS} = 4.5V		-	1.6	2.2	
		I_D = 80A, V_{GS} = 10V T_J = 175°C		-	2.3	3.0	
Dynamic Characteristics							
C_{iss}	Input Capacitance	V_{DS} = 15V, V_{GS} = 0V, f = 1MHz		-	11400	-	pF
C_{oss}	Output Capacitance			-	2140	-	pF
C_{rss}	Reverse Transfer Capacitance			-	1260	-	pF
R_G	Gate Resistance	V_{GS} = 0.5V, f = 1MHz		-	1.2	-	Ω
$Q_{g(TOT)}$	Total Gate Charge at 10V	V_{GS} = 0 to 10V	V_{DD} = 15V I_D = 80A I_g = 1.0mA	-	204	265	nC
$Q_{g(5)}$	Total Gate Charge at 5V	V_{GS} = 0 to 5V		-	100	130	nC
$Q_{g(TH)}$	Threshold Gate Charge	V_{GS} = 0 to 1V		-	10.9	14.2	nC
Q_{gs}	Gate to Source Gate Charge			-	33	-	nC
Q_{gs2}	Gate Charge Threshold to Plateau			-	22	-	nC
Q_{gd}	Gate to Drain "Miller" Charge			-	43	-	nC

Electrical Characteristics $T_J = 25°C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Switching Characteristics						
$t_{(on)}$	Turn-On Time		-	-	155	ns
$t_{d(on)}$	Turn-On Delay Time		-	24	-	ns
t_r	Turn-On Rise Time	$V_{DD} = 15V$, $I_D = 80A$	-	73	-	ns
$t_{d(off)}$	Turn-Off Delay Time	$V_{GS} = 5V$, $R_{GS} = 1.5\Omega$	-	54	-	ns
t_f	Turn-Off Fall Time		-	38	-	ns
t_{off}	Turn-Off Time		-	-	149	ns
Drain-Source Diode Characteristics						
V_{SD}	Source to Drain Diode Voltage	$I_{SD} = 75A$	-	0.8	1.25	V
		$I_{SD} = 40A$	-	0.8	1.0	V
t_{rr}	Reverse Recovery Time	$I_F = 75A$, di/dt = 100A/μs	-	59	77	ns
Q_{rr}	Reverse Recovery Charge	$I_F = 75A$, di/dt = 100A/μs	-	67	87	nC

Notes:
1: Starting $T_J = 25°C$, L = 0.61mH, $I_{AS} = 64A$, $V_{DD} = 30V$, $V_{GS} = 10V$.
2: Pulse width = 100s.

This product has been designed to meet the extreme test conditions and environment demanded by the automotive industry. For a copy of the requirements, see AEC Q101 at: http://www.aecouncil.com/
All Fairchild Semiconductor products are manufactured, assembled and tested under ISO9000 and QS9000 quality systems certification.
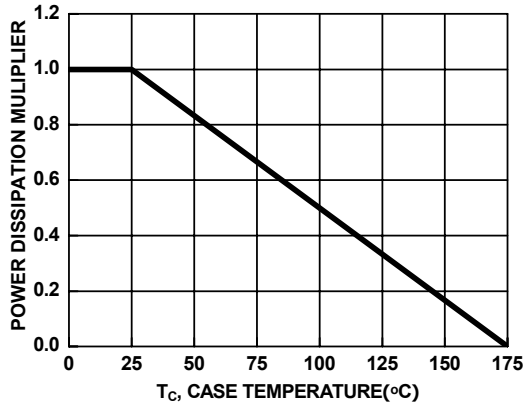
Typical Characteristics

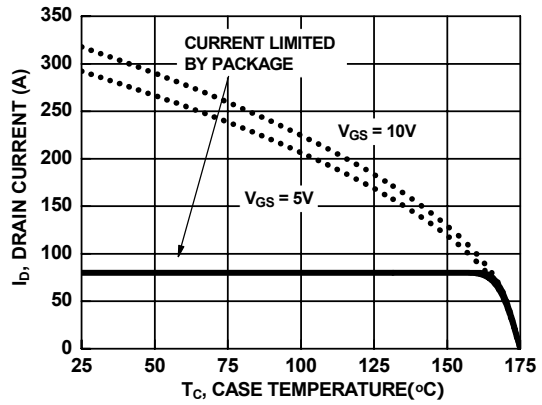Figure 1. Normalized Power Dissipation vs Case Temperature

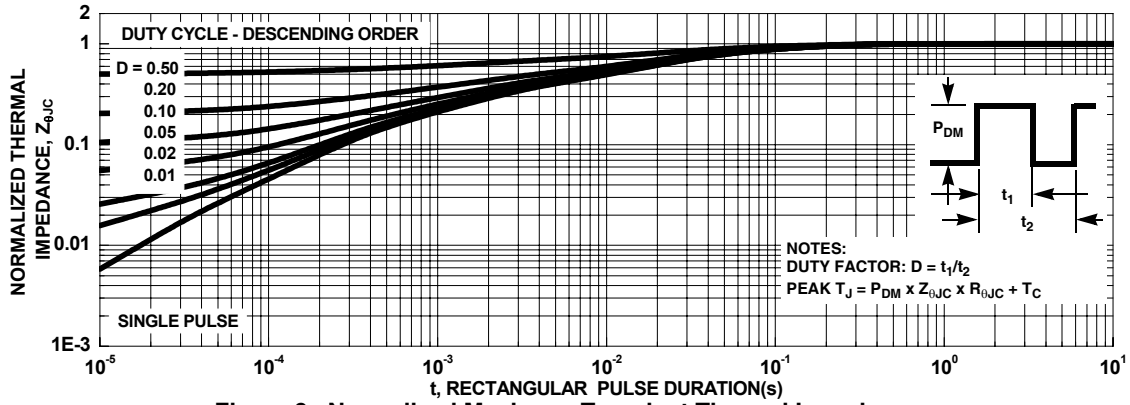Figure 2. Maximum Continuous Drain Current vs Case Temperature

NOTES:
DUTY FACTOR: $D = t_1/t_2$
PEAK $T_J = P_{DM}$ x $Z_{\theta JC}$ x $R_{\theta JC}$ + T_C

Figure 3. Normalized Maximum Transient Thermal Impedance

$T_C = 25^oC$
FOR TEMPERATURES ABOVE 25°C DERATE PEAK CURRENT AS FOLLOWS:

$$I = I_{25} \left[\sqrt{\frac{175 - T_C}{150}} \right]$$
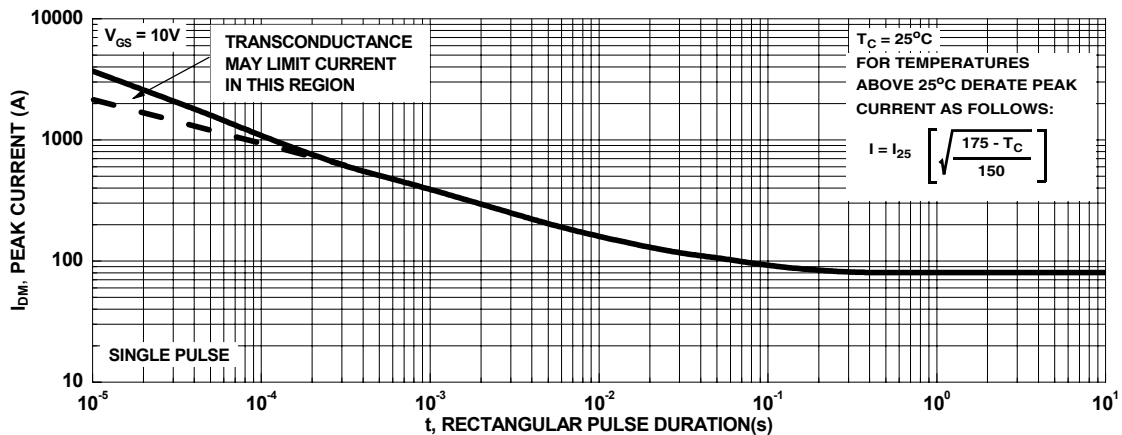
Figure 4. Peak Current Capability

Typical Characteristics
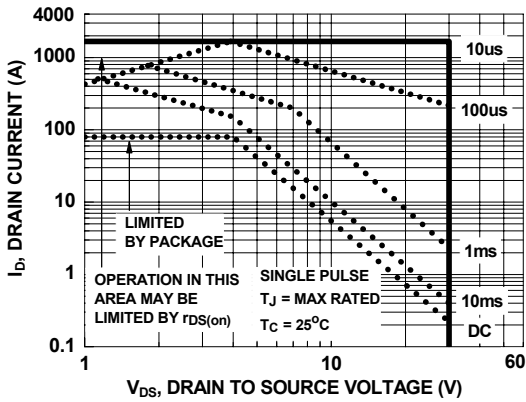
Figure 5. Forward Bias Safe Operating Area
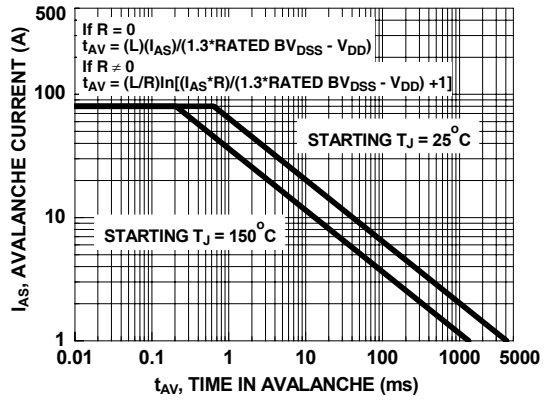
NOTE: Refer to Fairchild Application Notes AN7514 and AN7515
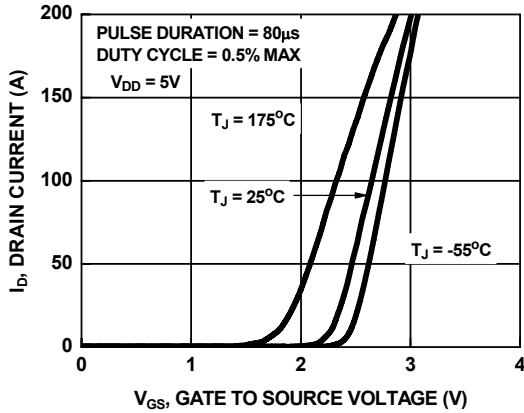
Figure 6. Unclamped Inductive Switching Capability
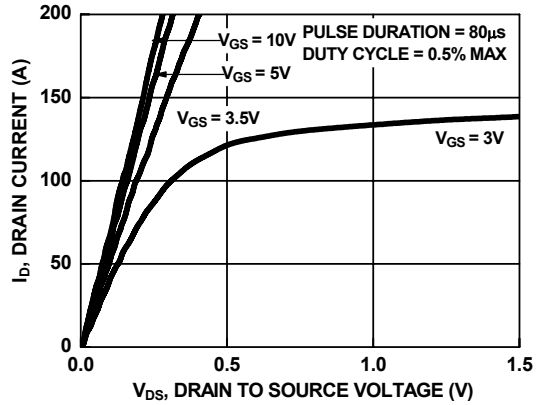

Figure 7. Transfer Characteristics
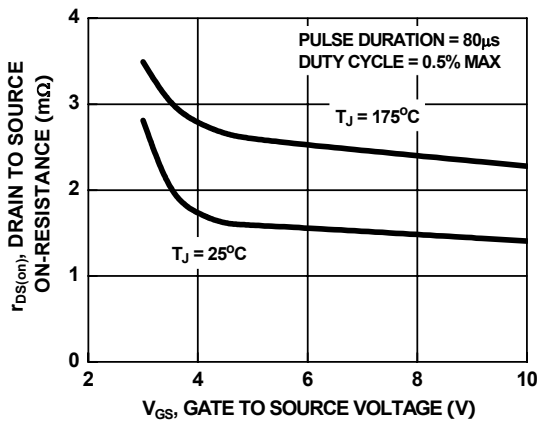

Figure 8. Saturation Characteristics

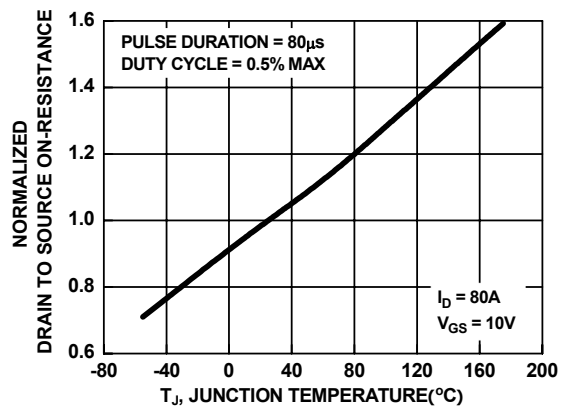**Figure 9. Drain to Source On-Resistance Variation vs Gate to Source Voltage**

Figure 10. Normalized Drain to Source On Resistance vs Junction Temperature
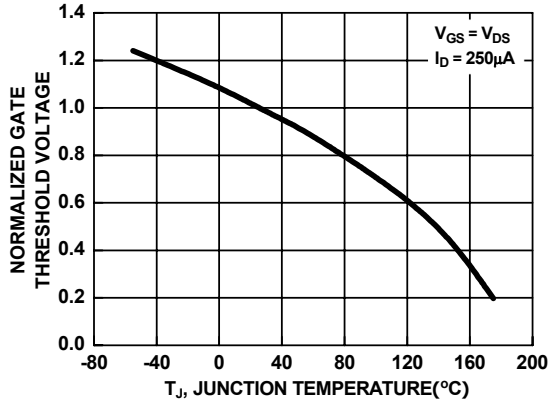
Typical Characteristics

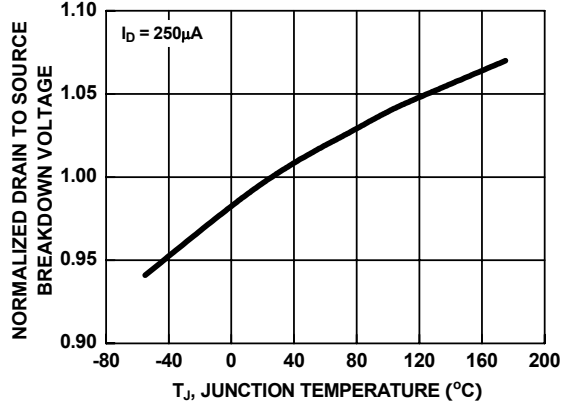Figure 11. Normalized Gate Threshold Voltage vs Junction Temperature
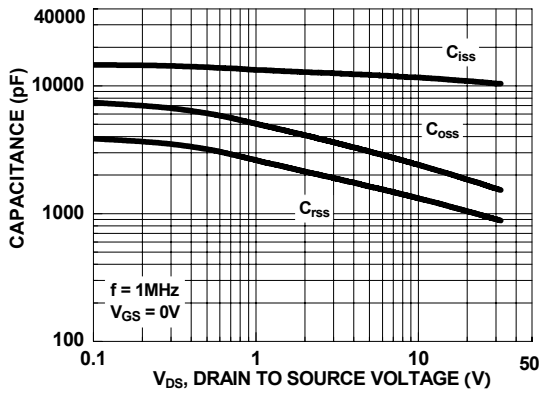
Figure 12. Normalized Drain to Source Breakdown Voltage vs Junction Temperature

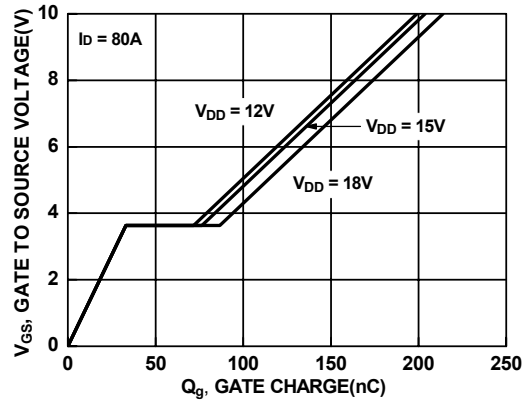Figure 13. Capacitance vs Drain to Source Voltage

Figure 14. Gate Charge vs Gate to Source Voltage

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