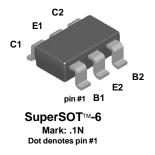


## FMBA14



## **NPN Multi-Chip Darlington Transistor**

This device is designed for applications requiring extremely high current gain at collector currents to 1.0 A. Sourced from Process 05.

#### Absolute Maximum Ratings\* T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V <sub>CES</sub>	Collector-Emitter Voltage	30	V	
V <sub>CBO</sub>	Collector-Base Voltage	30	V	
V <sub>EBO</sub>	Emitter-Base Voltage	10	V	
I <sub>C</sub>	Collector Current - Continuous	1.2	A	
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C	

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

#### **Thermal Characteristics** T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units	
		FMBA14		
P <sub>D</sub>	Total Device Dissipation	700	mW	
	Derate above 25°C	5.6	mW/°C	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	180	°C/W	

## **NPN Multi-Chip Darlington Transistor**

(continued)

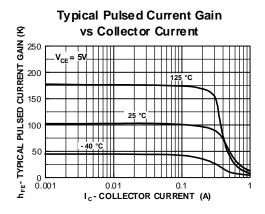
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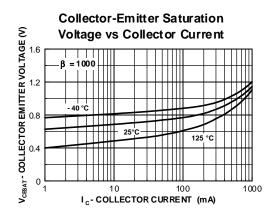
TA = 25°C unless otherwise noted

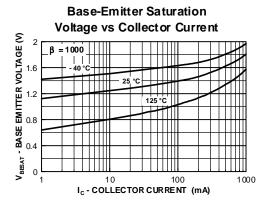
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
OEE CHAI	OFF CHARACTERISTICS					
			1			
$V_{(BR)CES}$	Collector-Emitter Breakdown Voltage	$I_C = 100  \mu A, I_B = 0$	30			V
I <sub>CBO</sub>	Collector-Cutoff Current	$V_{CB} = 30 \text{ V}, I_{E} = 0$			100	nA
I <sub>EBO</sub>	Emitter-Cutoff Current	$V_{EB} = 10 \text{ V}, I_{C} = 0$			100	nA
ON CHAR	ACTERISTICS*  DC Current Gain	I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 5.0 V I <sub>C</sub> = 100 mA, V <sub>CE</sub> = 5.0 V	10K 20K			
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$I_C = 100 \text{ mA}, V_C = 0.0 \text{ V}$	2010		1.5	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = 100 mA, V <sub>CE</sub> = 5.0 V			2.0	V
<del></del>	SMALL SIGNAL CHARACTERISTICS					
h <sub>fe</sub>	Small Signal Current Gain	$I_C = 10 \text{ mA}, V_{CE} = 5.0 \text{ V},$ f = 100  MHz	1.25			MHz

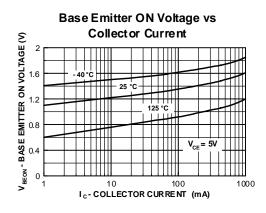
<sup>\*</sup>Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%

## **Typical Characteristics**







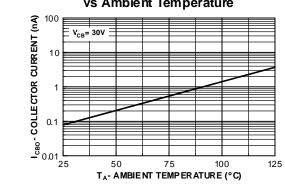


## **NPN Multi-Chip Darlington Transistor**

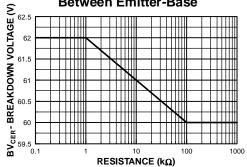
(continued)

### Typical Characteristics (continued)

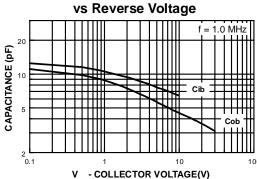
# Collector-Cutoff Current vs Ambient Temperature



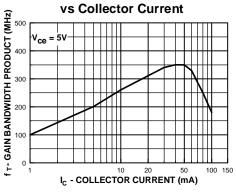
### Collector-Emitter Breakdown Voltage with Resistance Between Emitter-Base



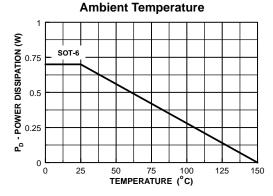
## Input and Output Capacitance



## Gain Bandwidth Product



## Power Dissipation vs



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