

PBSS4021NX

20 V, 7 A NPN low V_{CEsat} (BISS) transistor

11 December 2012

Product data sheet

1. General description

NPN low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in a medium power and flat lead SOT89 (SC-62) Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS4021PX.

2. Features and benefits

- Very low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- High energy efficiency due to less heat generation
- AEC-Q101 qualified
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors

3. Applications

- Loadswitch
- Battery-driven devices
- Power management
- Charging circuits
- Power switches (e.g. motors, fans)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	20	V
I _C	collector current		-	-	7	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	15	A
R _{CEsat}	collector-emitter saturation resistance	I _C = 5 A; I _B = 500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	19	28	mΩ

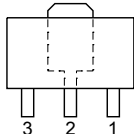
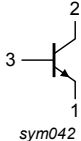


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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 <p style="text-align: center;">SOT89</p>	 <p style="text-align: center;">sym042</p>
2	C	collector		
3	B	base		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4021NX	SOT89	plastic surface-mounted package; die pad for good heat transfer; 3 leads	SOT89

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4021NX	%6D

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit	
V_{CBO}	collector-base voltage	open emitter	-	20	V	
V_{CEO}	collector-emitter voltage	open base	-	20	V	
V_{EBO}	emitter-base voltage	open collector	-	5	V	
I_C	collector current		-	7	A	
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms	-	15	A	
I_B	base current		-	1	A	
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	600	mW
			[2]	-	1650	mW
			[3]	-	2500	mW

Symbol	Parameter	Conditions	Min	Max	Unit
T _j	junction temperature		-	150	°C
T _{amb}	ambient temperature		-55	150	°C
T _{stg}	storage temperature		-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

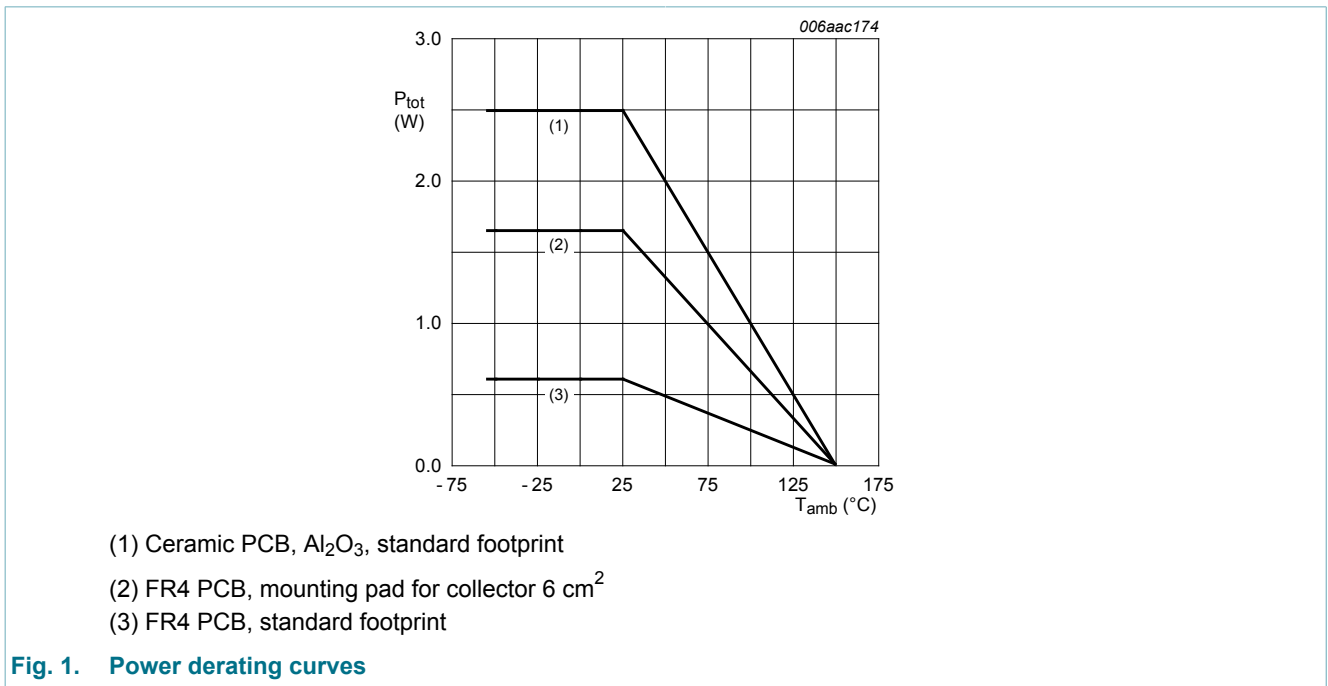


Fig. 1. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	[1] in free air	-	-	210	K/W
		[2]	-	-	75	K/W
		[3]	-	-	50	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		-	-	20	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

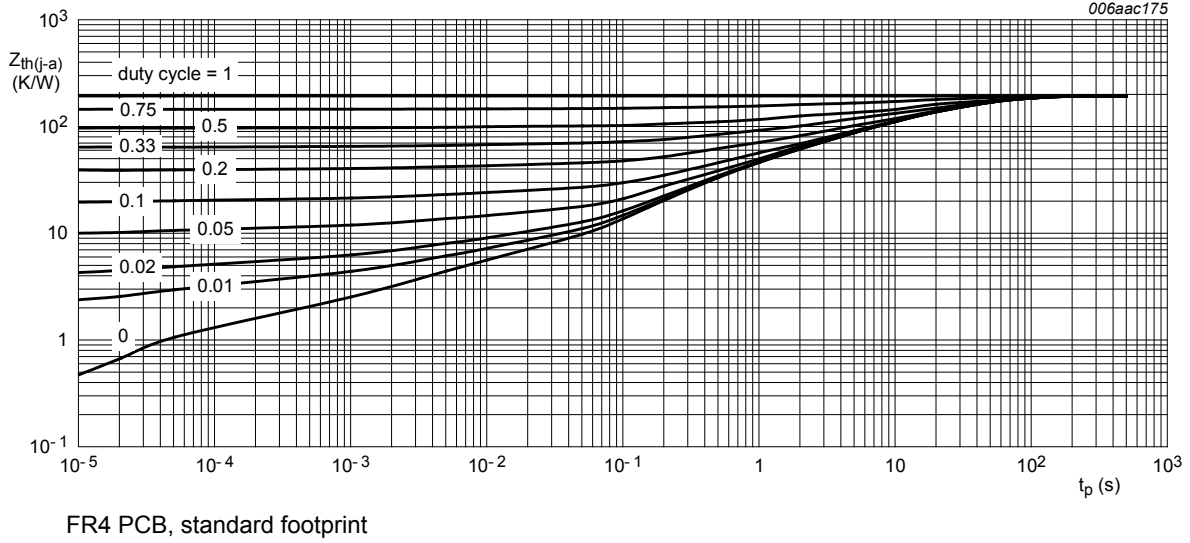


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

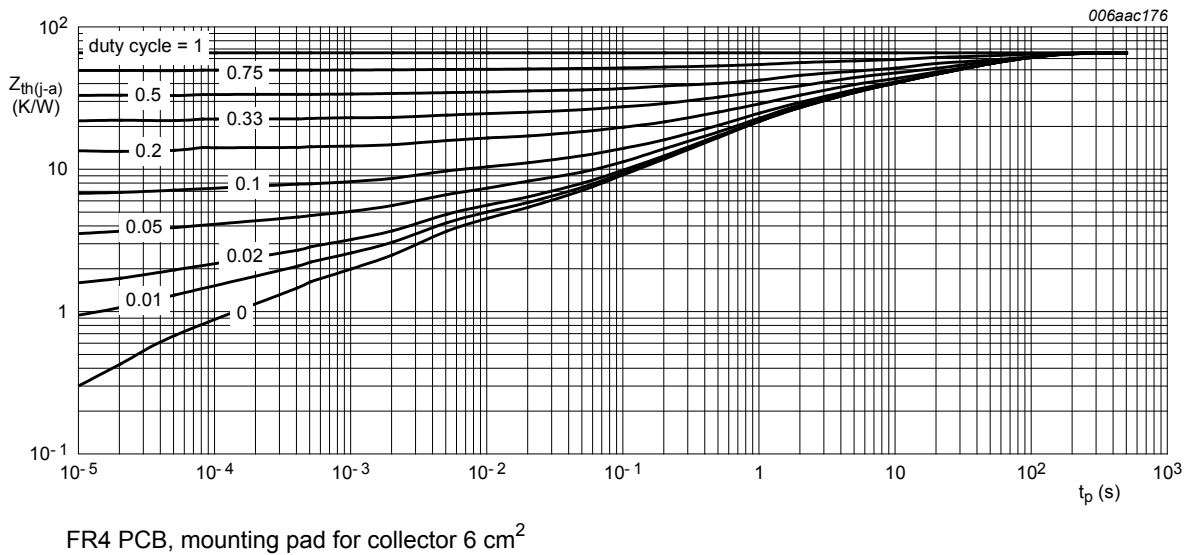
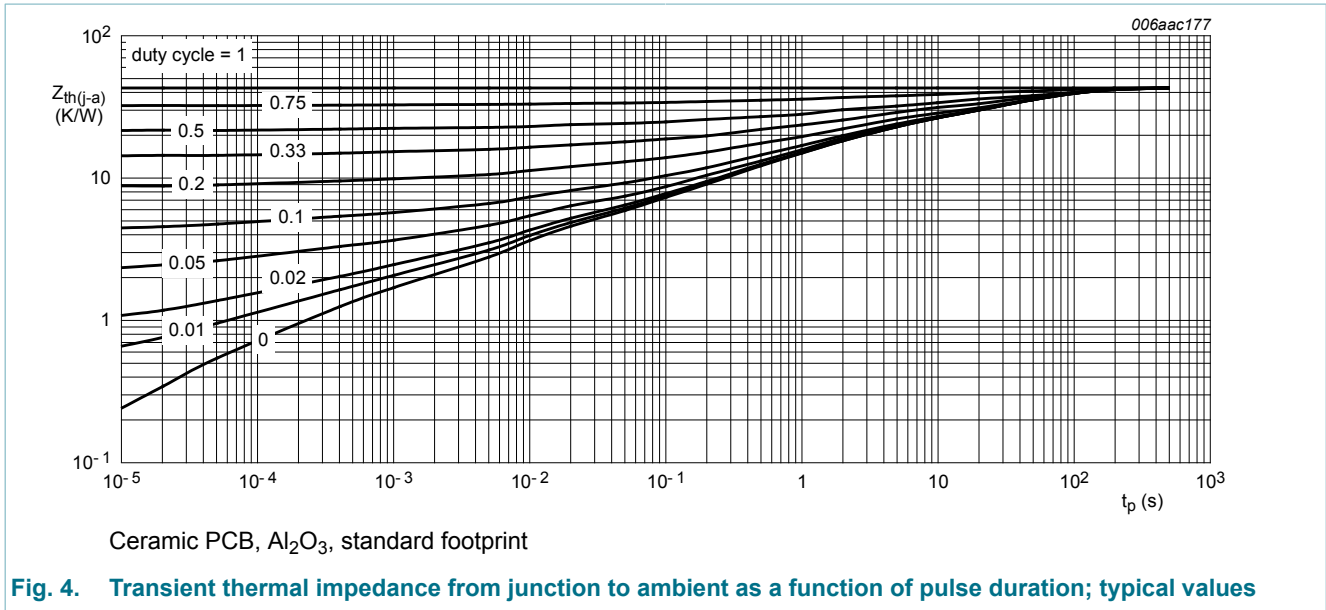


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 20 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
		V _{CB} = 20 V; I _E = 0 A; T _j = 150 °C	-	-	50	μA
I _{CES}	collector-emitter cut-off current	V _{CE} = 16 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	300	550	-	
		V _{CE} = 2 V; I _C = 1 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	300	550	-	
		V _{CE} = 2 V; I _C = 2 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	300	500	-	
		V _{CE} = 2 V; I _C = 4 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	250	450	-	
		V _{CE} = 2 V; I _C = 8 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	100	200	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 1 A; I _B = 50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	25	38	mV
		I _C = 1 A; I _B = 10 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	35	60	mV

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
		$I_C = 2\text{ A}; I_B = 40\text{ mA};$ pulsed; $t_p \leq 300\ \mu\text{s}; \delta \leq 0.02; T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	48	75	mV
		$I_C = 4\text{ A}; I_B = 200\text{ mA};$ pulsed; $t_p \leq 300\ \mu\text{s}; \delta \leq 0.02; T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	78	120	mV
		$I_C = 4\text{ A}; I_B = 40\text{ mA};$ pulsed; $t_p \leq 300\ \mu\text{s}; \delta \leq 0.02; T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	85	140	mV
		$I_C = 7\text{ A}; I_B = 350\text{ mA};$ pulsed; $t_p \leq 300\ \mu\text{s}; \delta \leq 0.02; T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	137	210	mV
R_{CEsat}	collector-emitter saturation resistance	$I_C = 5\text{ A}; I_B = 500\text{ mA};$ pulsed; $t_p \leq 300\ \mu\text{s}; \delta \leq 0.02; T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	19	28	m Ω
V_{BEsat}	base-emitter saturation voltage	$I_C = 1\text{ A}; I_B = 100\text{ mA};$ pulsed; $t_p \leq 300\ \mu\text{s}; \delta \leq 0.02; T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	0.82	0.9	V
		$I_C = 4\text{ A}; I_B = 400\text{ mA};$ pulsed; $t_p \leq 300\ \mu\text{s}; \delta \leq 0.02; T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	0.92	1.05	V
V_{BEon}	base-emitter turn-on voltage	$V_{\text{CE}} = 2\text{ V}; I_C = 2\text{ A};$ pulsed; $t_p \leq 300\ \mu\text{s};$ $\delta \leq 0.02; T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	0.74	0.85	V
t_d	delay time	$V_{\text{CC}} = 12.5\text{ V}; I_C = 1\text{ A}; I_{\text{Bon}} = 0.05\text{ A};$ $I_{\text{Boff}} = -0.05\text{ A}; T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	40	-	ns
t_r	rise time		-	40	-	ns
t_{on}	turn-on time		-	80	-	ns
t_s	storage time		-	650	-	ns
t_f	fall time		-	75	-	ns
t_{off}	turn-off time	$V_{\text{CC}} = 12.5\text{ V}; I_C = 1\text{ A}; I_{\text{Bon}} = 0.05\text{ A};$ $I_{\text{Boff}} = -0.05\text{ A}; T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	725	-	ns
f_T	transition frequency	$V_{\text{CE}} = 10\text{ V}; I_C = 100\text{ mA}; f = 100\text{ MHz};$ $T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	115	-	MHz
C_c	collector capacitance	$V_{\text{CB}} = 10\text{ V}; I_E = 0\text{ A}; i_e = 0\text{ A};$ $f = 1\text{ MHz}; T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	85	-	pF

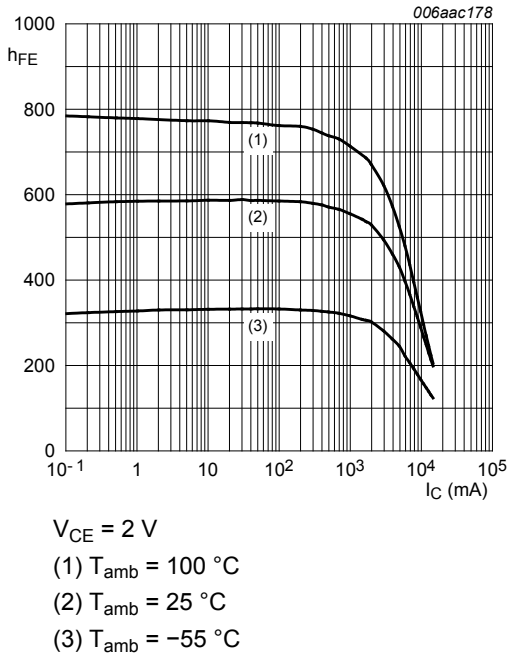


Fig. 5. DC current gain as a function of collector current; typical values

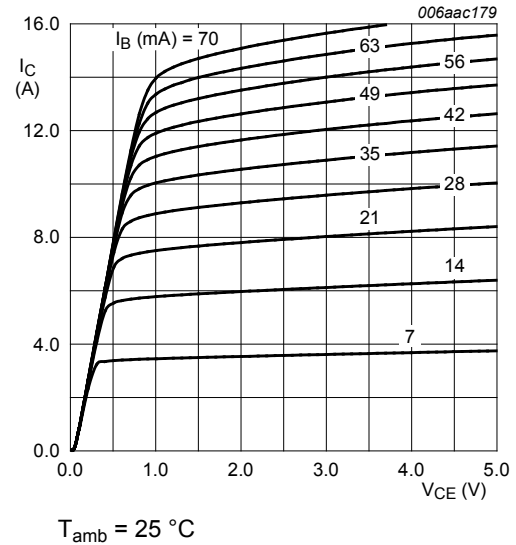


Fig. 6. Collector current as a function of collector-emitter voltage; typical values

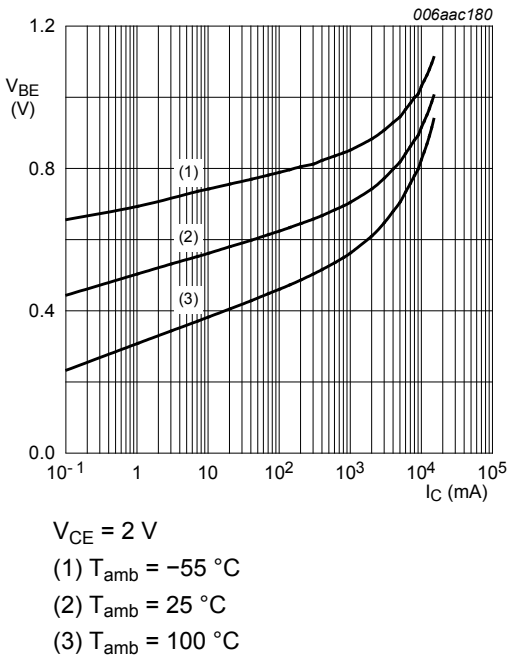


Fig. 7. Base-emitter voltage as a function of collector current; typical values

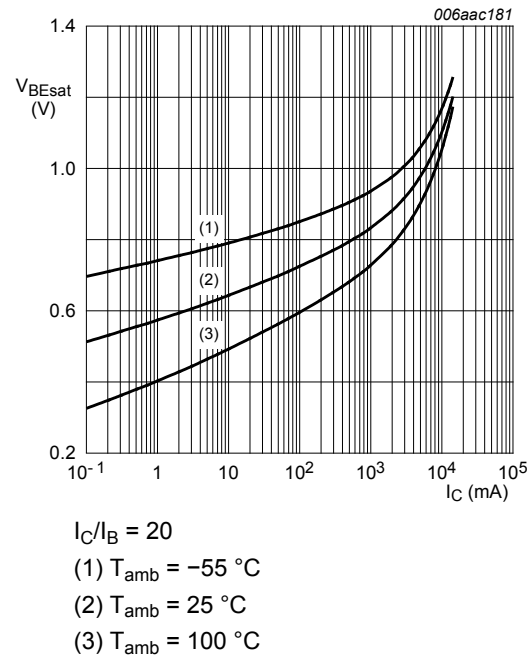
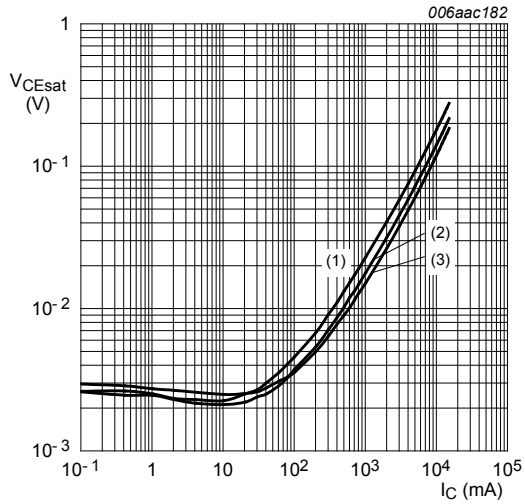
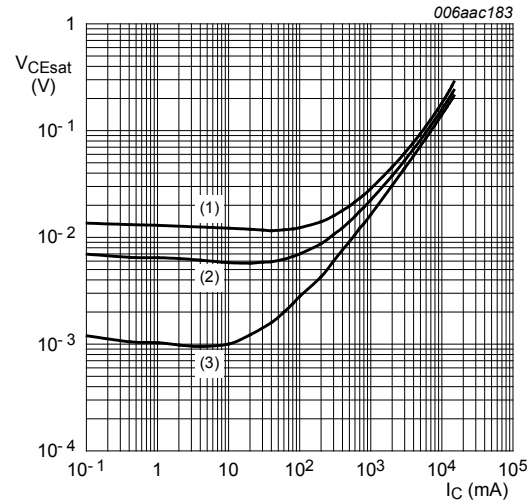


Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values



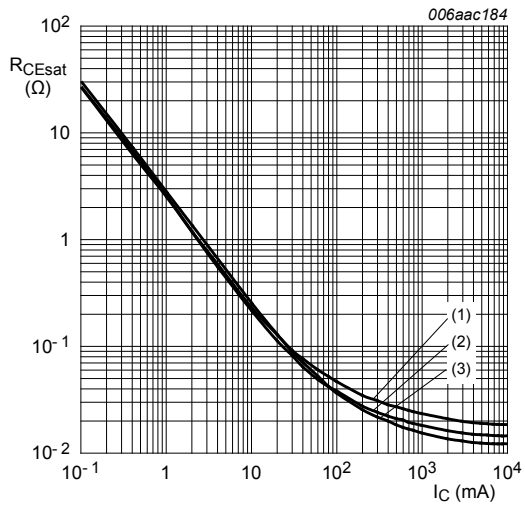
$I_C/I_B = 20$
 (1) $T_{amb} = 100\text{ }^\circ\text{C}$
 (2) $T_{amb} = 25\text{ }^\circ\text{C}$
 (3) $T_{amb} = -55\text{ }^\circ\text{C}$

Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values



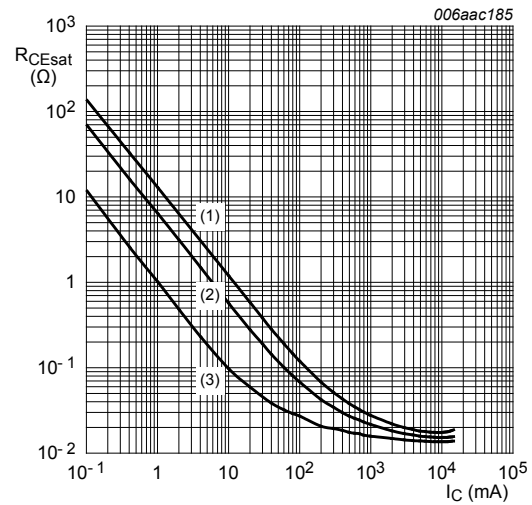
$T_{amb} = 25\text{ }^\circ\text{C}$
 (1) $I_C/I_B = 100$
 (2) $I_C/I_B = 50$
 (3) $I_C/I_B = 10$

Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values



$I_C/I_B = 20$
 (1) $T_{amb} = 100\text{ }^\circ\text{C}$
 (2) $T_{amb} = 25\text{ }^\circ\text{C}$
 (3) $T_{amb} = -55\text{ }^\circ\text{C}$

Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values



$T_{amb} = 25\text{ }^\circ\text{C}$
 (1) $I_C/I_B = 100$
 (2) $I_C/I_B = 50$
 (3) $I_C/I_B = 10$

Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

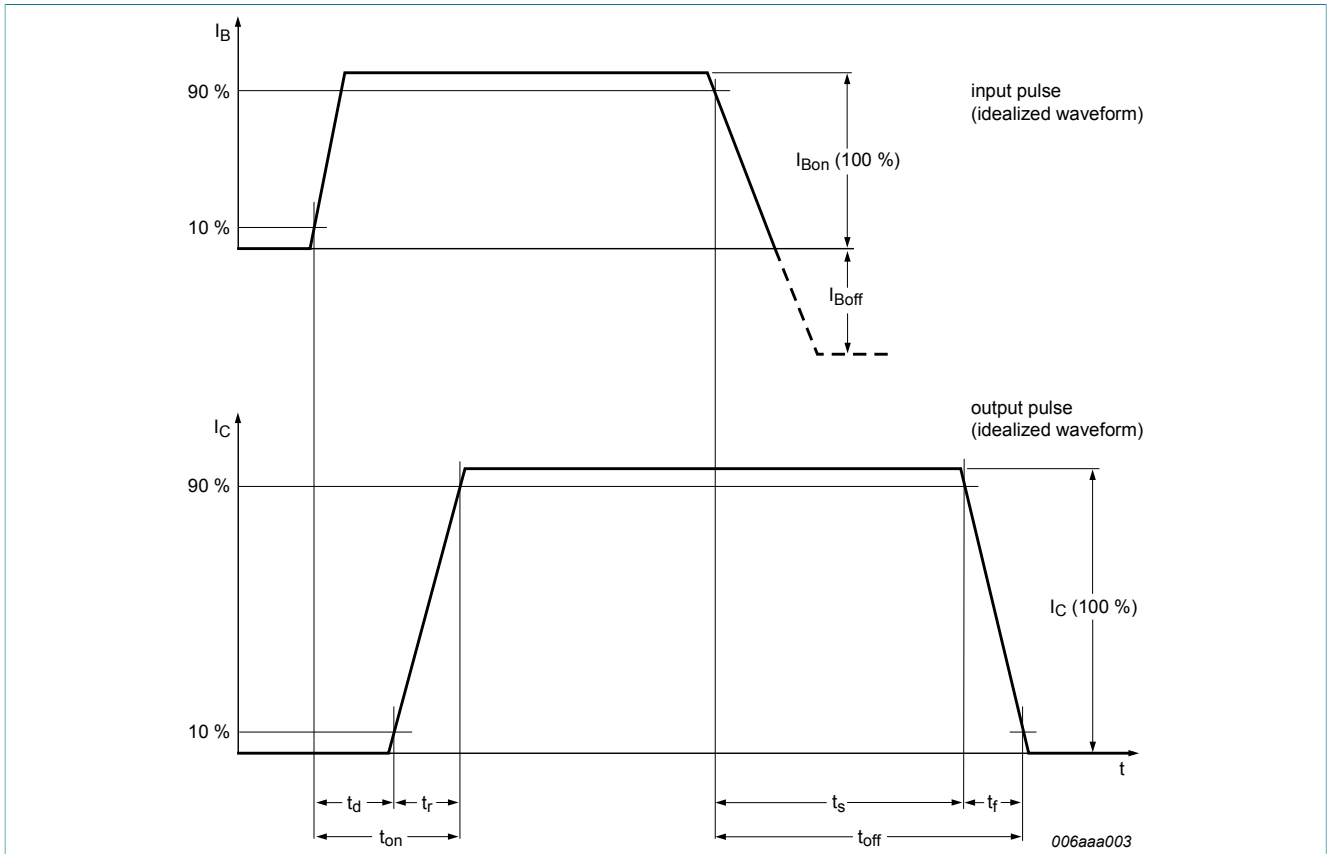


Fig. 13. BISS transistor switching time definition

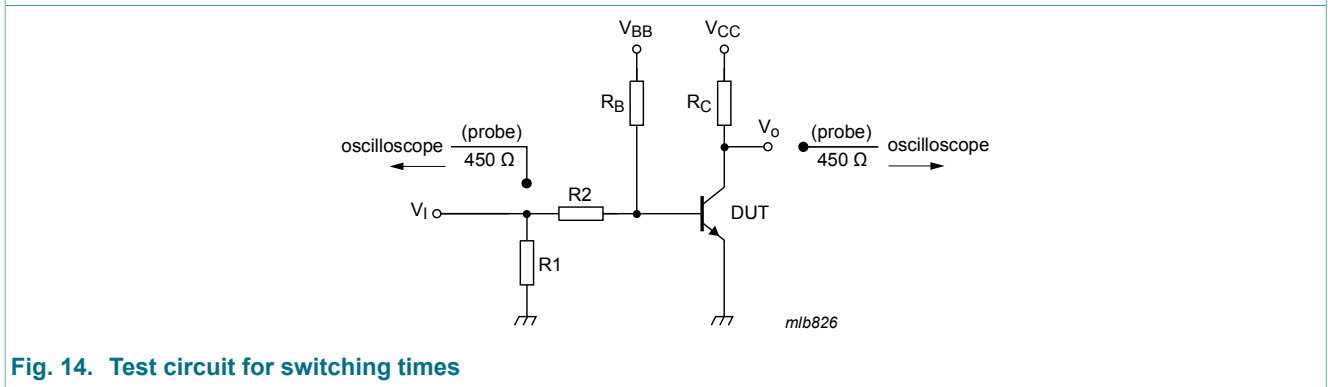


Fig. 14. Test circuit for switching times

11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

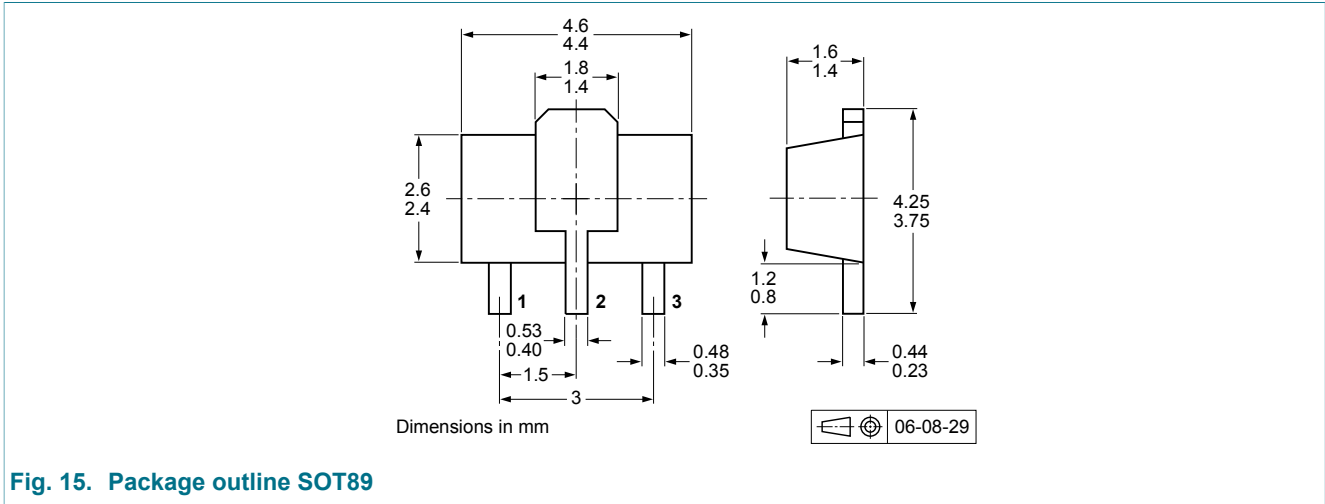


Fig. 15. Package outline SOT89

13. Soldering

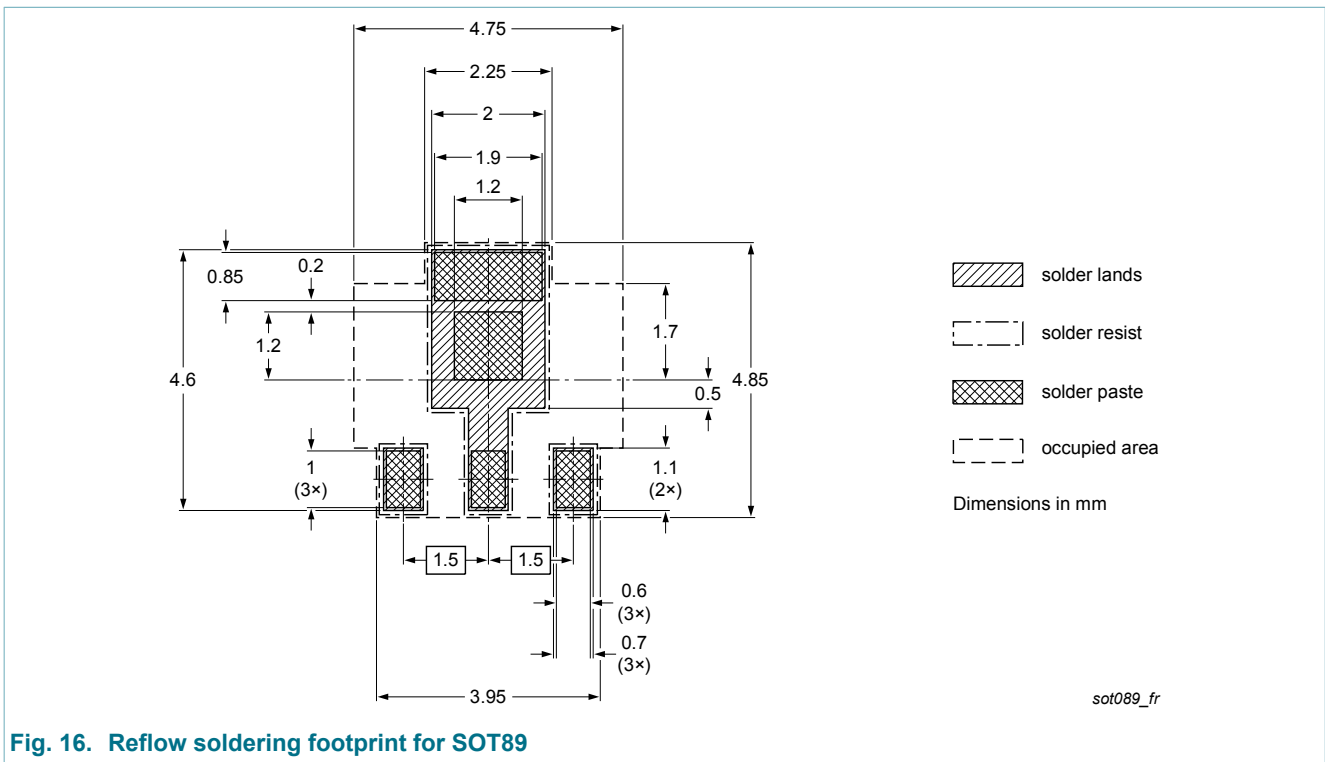
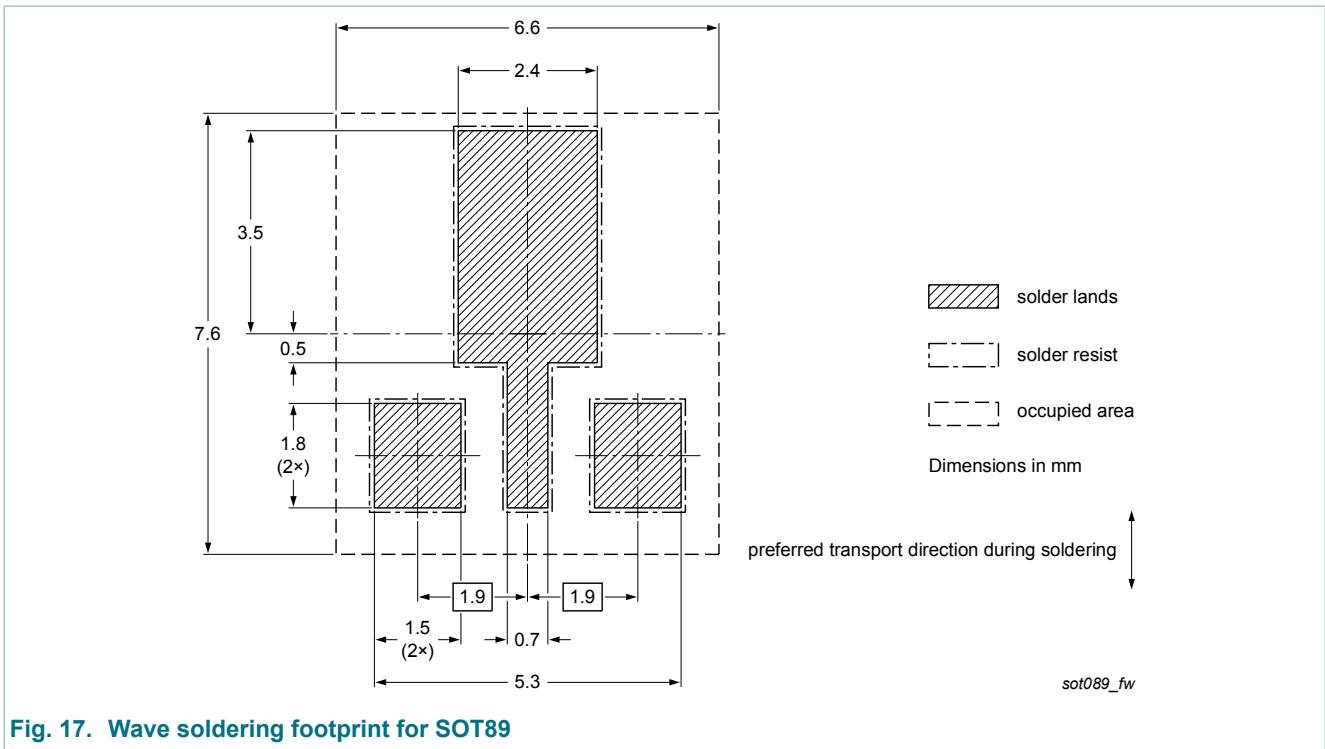


Fig. 16. Reflow soldering footprint for SOT89



14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4021NX v.3	20121211	Product data sheet	-	PBSS4021NX v.2
Modifications:	<ul style="list-style-type: none"> Editorial update 			
PBSS4021NX v.2	20121009	Product data sheet	-	PBSS4021NX v.1
PBSS4021NX v.1	20100401	Product data sheet	-	-

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15.1 Data sheet status

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Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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