



CPH6153

Bipolar Transistor -20V, -3A, Low VCE(sat), PNP Single CPH6

ON Semiconductor®

<http://onsemi.com>

Applications

- Load switch, DC-DC converter, motor drivers, charger

Features

- Adoption of MBIT process
- Large current capacity
- Low collector-to-emitter saturation voltage
- High speed switching
- Ultrasmall-sized package permitting applied sets to be made small and slim (0.9mm)
- High allowable power dissipation
- IECO is guaranteed for preventing reverse flow from the collector to the emitter

Specifications

Absolute Maximum Ratings at Ta=25°C

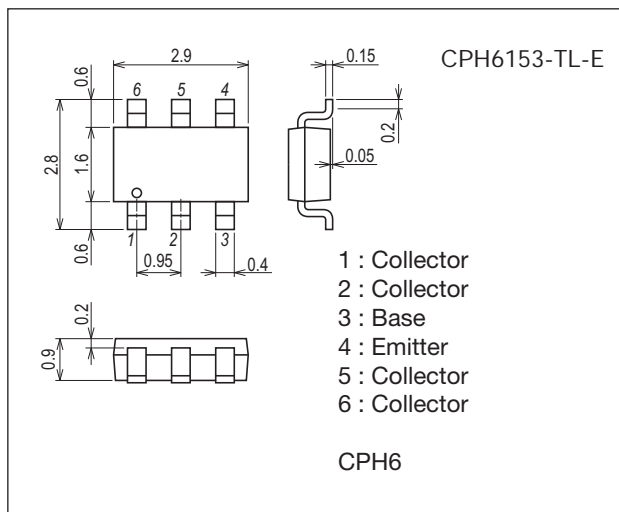
| Parameter | Symbol | Conditions | Ratings | Unit |
|------------------------------|------------------|---|-------------|------|
| Collector-to-Base Voltage | V _{CBO} | | -20 | V |
| Collector-to-Emitter Voltage | V _{CEO} | | -20 | V |
| Emitter-to-Base Voltage | V _{EBO} | | -5 | V |
| Collector Current | I _C | | -3 | A |
| Collector Current (Pulse) | I _{CP} | | -5 | A |
| Base Current | I _B | | -600 | mA |
| Collector Dissipation | P _C | When mounted on ceramic substrate (600mm ² x0.8mm) | 1.3 | W |
| Junction Temperature | T _j | | 150 | °C |
| Storage Temperature | T _{stg} | | -55 to +150 | °C |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ)

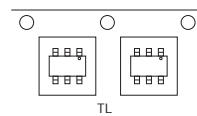
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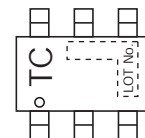
Product & Package Information

- Package : CPH6
- JEITA, JEDEC : SC-74, SOT-26, SOT-457
- Minimum Packing Quantity : 3,000 pcs./reel

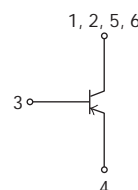
Packing Type: TL



Marking



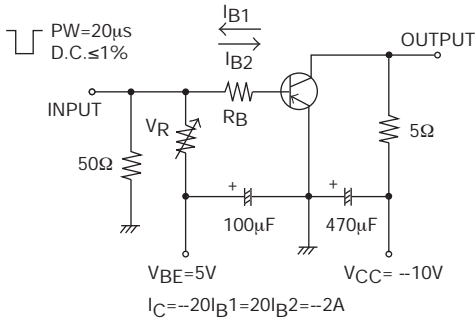
Electrical Connection



Electrical Characteristics at Ta=25°C

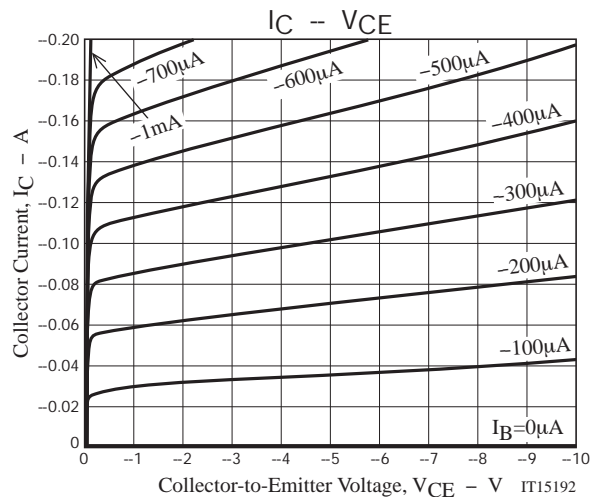
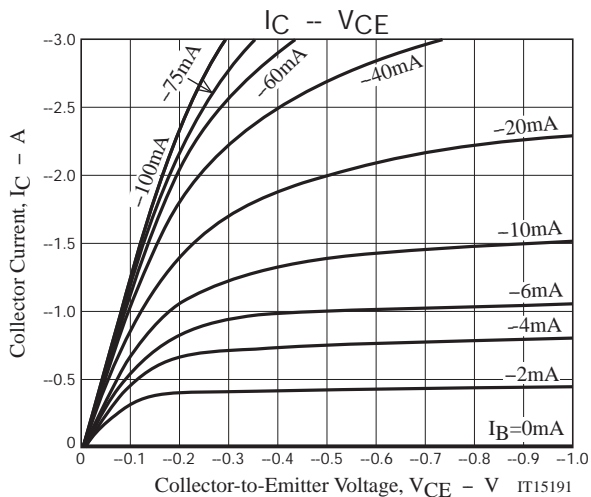
| Parameter | Symbol | Conditions | Ratings | | | Unit |
|---|---------------|-------------------------------|---------|-------|------|---------|
| | | | min | typ | max | |
| Collector Cutoff Current | I_{CBO} | $V_{CB} = -20V, I_E = 0A$ | | | -0.1 | μA |
| Emitter Cutoff Current | I_{EBO} | $V_{EB} = -4V, I_C = 0A$ | | | -0.1 | μA |
| Emitter Cutoff Current | I_{ECO} | $V_{EC} = -4.5V, I_B = 0A$ | | | -1 | μA |
| DC Current Gain | h_{FE} | $V_{CE} = -2V, I_C = -100mA$ | 200 | | 560 | |
| Gain-Bandwidth Product | f_T | $V_{CE} = -10V, I_C = -300mA$ | | 400 | | MHz |
| Output Capacitance | C_{ob} | $V_{CB} = -10V, f = 1MHz$ | | 22 | | pF |
| Collector-to-Emitter Saturation Voltage | $V_{CE(sat)}$ | $I_C = -1.5A, I_B = -75mA$ | | -130 | -195 | mV |
| Base-to-Emitter Saturation Voltage | $V_{BE(sat)}$ | $I_C = -1.5A, I_B = -75mA$ | | -0.93 | -1.2 | V |
| Collector-to-Base Breakdown Voltage | $V_{(BR)CBO}$ | $I_C = -10\mu A, I_E = 0A$ | -20 | | | V |
| Collector-to-Emitter Breakdown Voltage | $V_{(BR)CEO}$ | $I_C = -1mA, R_{BE} = \infty$ | -20 | | | V |
| Emitter-to-Base Breakdown Voltage | $V_{(BR)EBO}$ | $I_E = -10\mu A, I_C = 0A$ | -5 | | | V |
| Turn-On Time | t_{on} | See specified Test Circuit. | | 35 | | ns |
| Storage Time | t_{stg} | See specified Test Circuit. | | 65 | | ns |
| Fall Time | t_f | See specified Test Circuit. | | 12 | | ns |

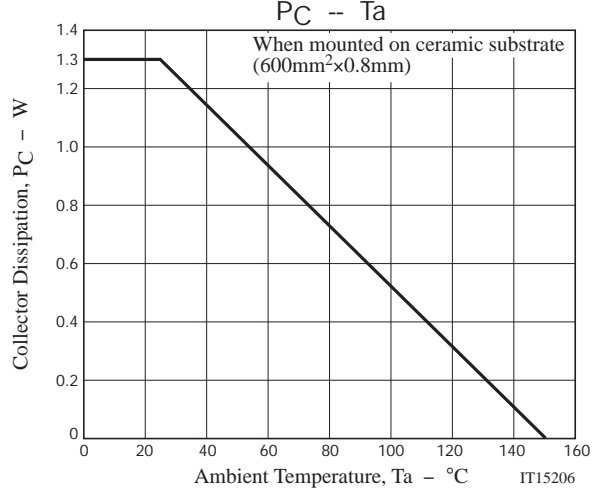
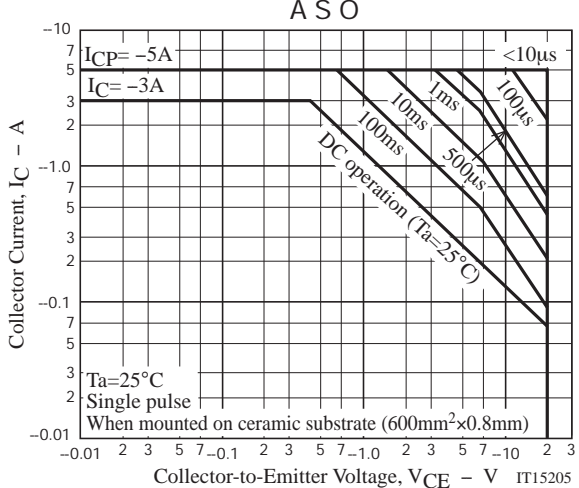
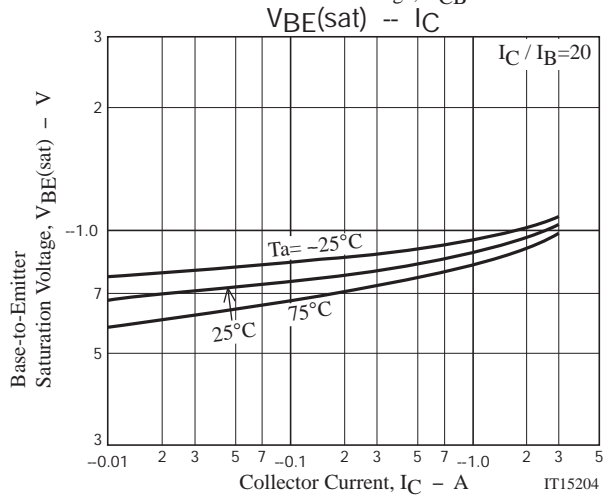
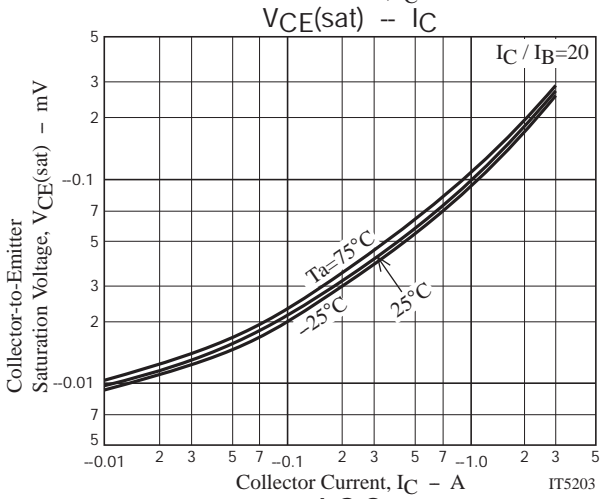
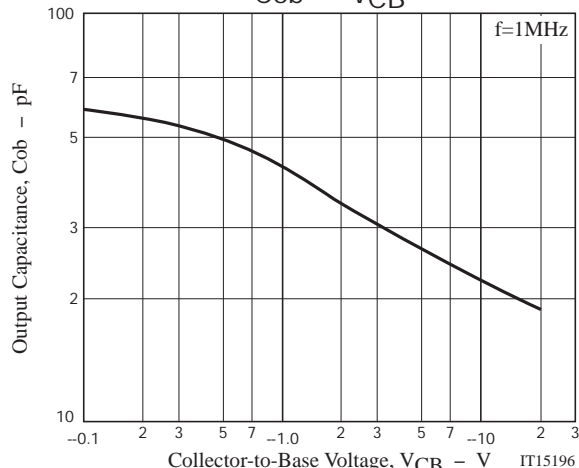
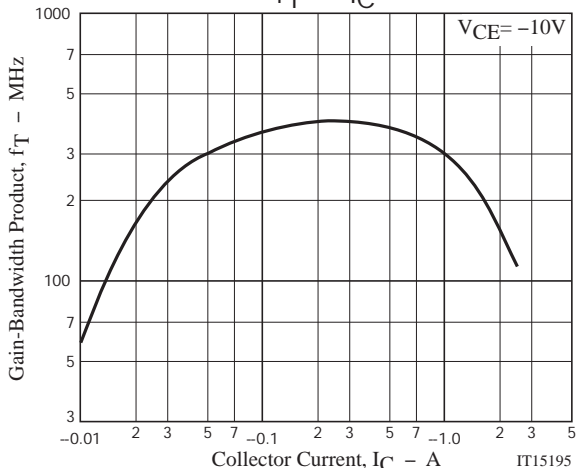
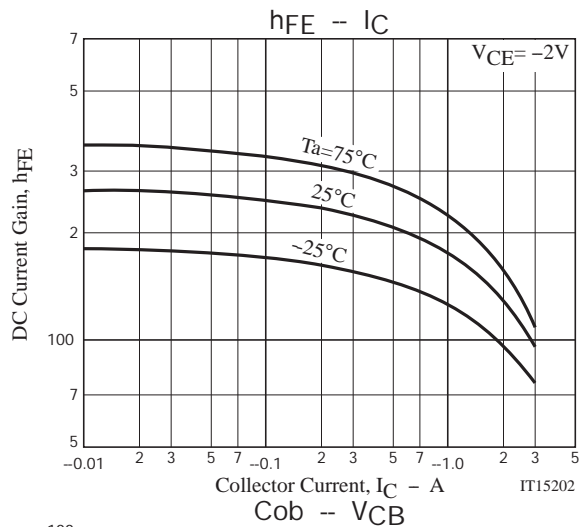
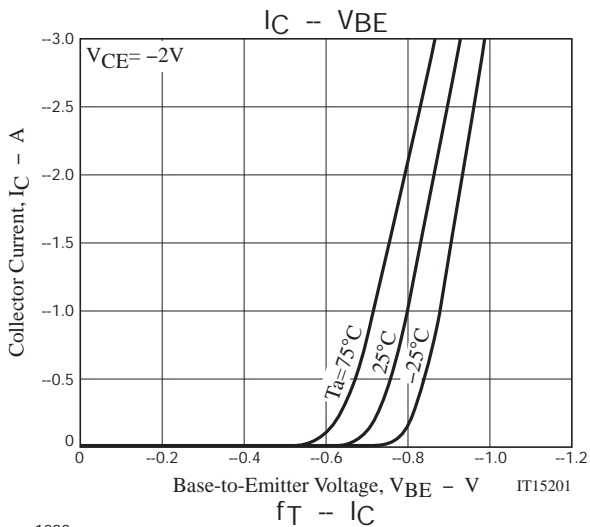
Switching Time Test Circuit



Ordering Information

| Device | Package | Shipping | memo |
|--------------|---------|----------------|---------|
| CPH6153-TL-E | CPH6 | 3,000pcs./reel | Pb Free |





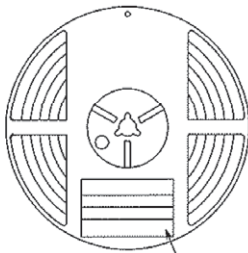
Embossed Taping Specification

CPH6153-TL-E

1. Packing Format

| Package Name | Carrier Tape Type | Maximum Number of devices contained (pcs) | | | Packing format | |
|--------------|-------------------|---|-----------|-----------|---|--|
| | | Reel | Inner box | Outer box | Inner BOX (C-1) | Outer BOX (A-7) |
| CPH6 | CPH6 | 3,000 | 15,000 | 90,000 | 5 reels contained Dimensions:mm (external) 183×72×185 | 6 inner boxes contained Dimensions:mm (external) 440×195×210 |

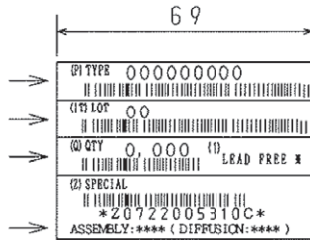
Packing method



Reel label

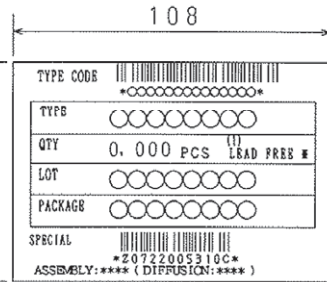
Type No.
LOT No.
Quantity
Origin

Reel label, Inner box label
(unit:mm)



Outer box label

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



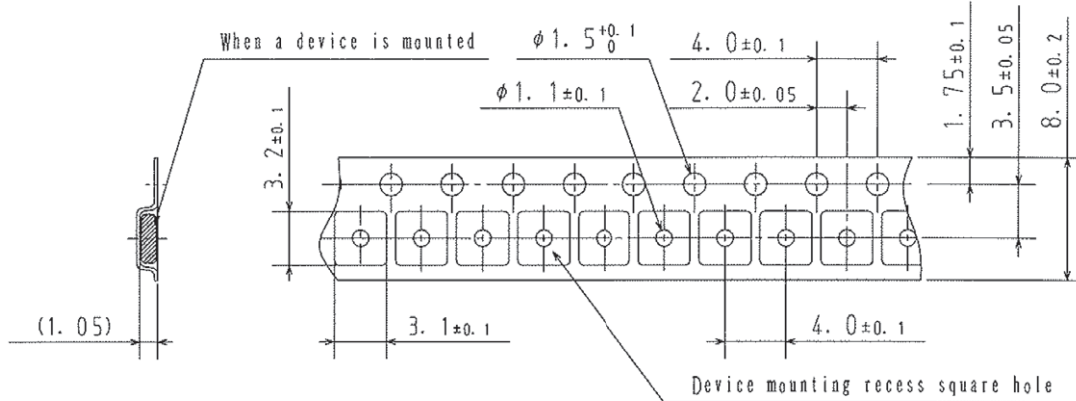
NOTE (1)

The LEAD FREE # description shows that the surface treatment of the terminal is lead free.

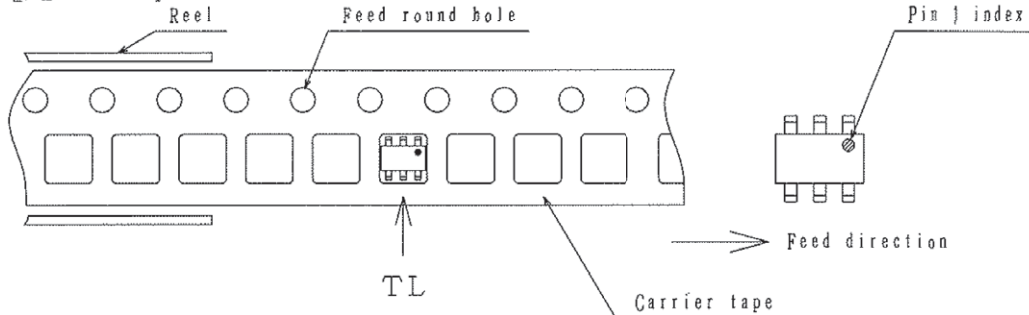
| Label | JEITA Phase |
|-------------|----------------|
| LEAD FREE 3 | JEITA Phase 3A |
| LEAD FREE 4 | JEITA Phase 3 |

2. Taping configuration

2-1. Carrier tape size (unit:mm)



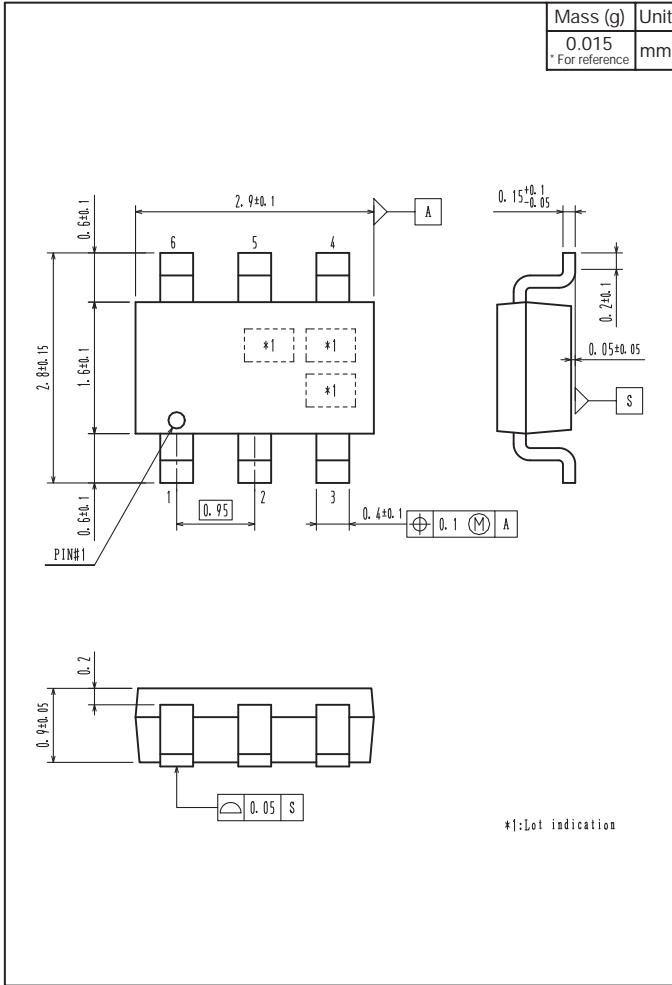
2-2. Device placement direction



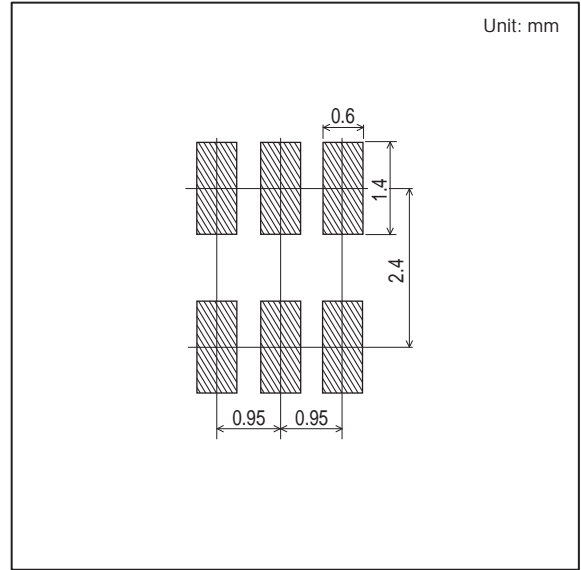
Those with pin 1 index on the feed hole side.....TL

CPH6153

Outline Drawing CPH6153-TL-E



Land Pattern Example



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