

Transistors with Built-in Resistor DRC2523E0L

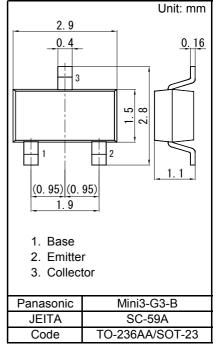
DRC2523E0L Silicon NPN epitaxial planar type

For digital circuits Complementary to DRA2523E

Features

- Low collector-emitter saturation voltage Vce(sat)
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)
- Marking Symbol: T1
- Packaging

Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)



Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	VCBO	50	V
Collector-emitter voltage (Base open)	VCEO	50	V
Collector current	IC	500	mA
Total power dissipation	PT	200	mW
Junction temperature	Tj	150	°C
Operating ambient temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-55 to +150	°C

$\begin{tabular}{|c|c|c|c|} \hline Internal Connection \\ \hline B & & & & \\ \hline B & & & & \\ \hline Resistance & R1 & 2.2 & k\Omega \\ \hline value & R2 & 2.2 & k\Omega \\ \hline \end{tabular}$

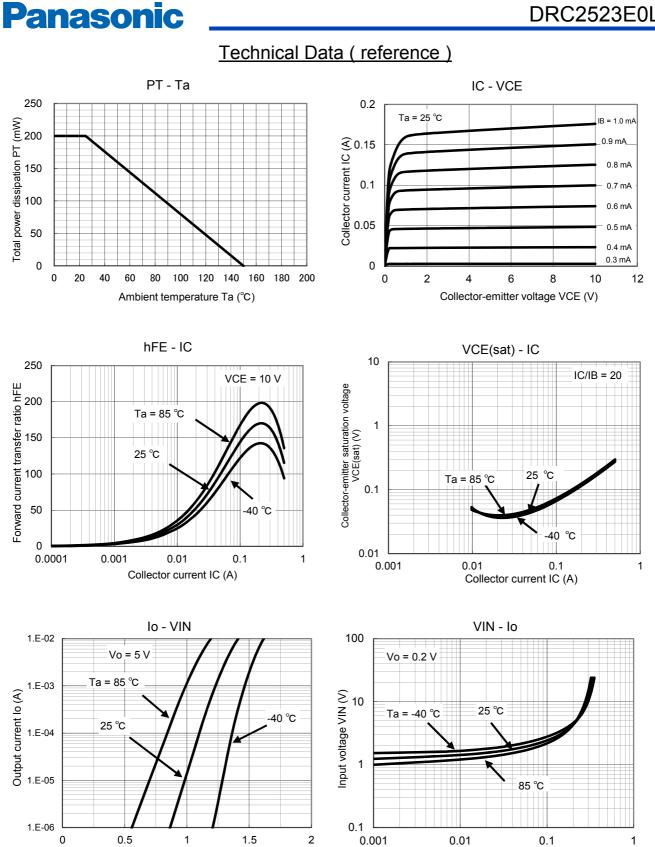
■ Electrical Characteristics Ta = 25 °C ± 3 °C

■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	VСВО	IC = 10 µA, IE = 0	50			V
Collector-emitter voltage (Base open)	VCEO	IC = 2 mA, IB = 0	50			V
Collector-base cutoff current (Emitter open)	ICBO	VCB = 50 V, IE = 0			1	μA
Collector-emitter cutoff current (Base open)	ICEO	VCE = 50 V, IB = 0			1	μA
Emitter-base cutoff current (Collector open)	IEBO	VEB = 6 V, IC = 0			5	mA
Forward current transfer ratio	hFE	VCE = 10 V, IC = 100 mA	40			-
Collector-emitter saturation voltage	VCE(sat)	IC = 100 mA, IB = 5 mA			0.25	V
Input voltage	Vi(on)	VCE = 0.2 V, IC = 50 mA	2.6			V
	Vi(off)	VCE = 5 V, IC = 100 µA			0.7	V
Input resistance	R1		-30%	2.2	+30%	kΩ
Resistance ratio	R1/R2		0.8	1.0	1.2	-

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

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Output current Io (A)

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Input voltage VIN (V)

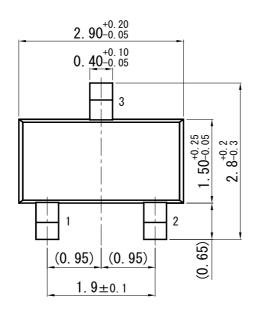
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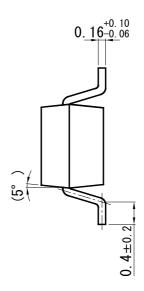


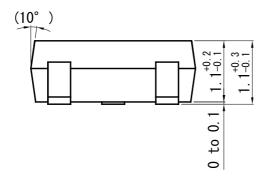
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Mini3-G3-B

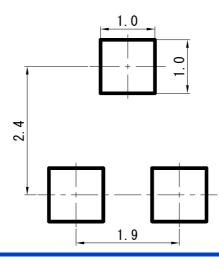
Unit: mm







Land Pattern (Reference) (Unit: mm)



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