DSC2002

Silicon NPN epitaxial planar type

For general amplification Complementary to DSA2002

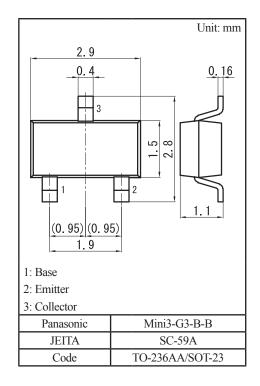
Features

- Low collector-emitter saturation voltage V_{CE(sat)}
- \bullet High forward current transfer ratio h_{FE} with excellent linearity
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)
- Marking Symbol: C2

Packaging

DSC2002×0L Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

Absolute Maximum Ratings $T_a = 25^{\circ}C$							
Parameter	Symbol	Rating	Unit				
Collector-base voltage (Emitter open)	V _{CBO}	60	V				
Collector-emitter voltage (Base open)	V _{CEO}	50	V				
Emitter-base voltage (Collector open)	V _{EBO}	5	V				
Collector current	I _C	500	mA				
Peak collector current	I _{CP}	1	А				
Collector power dissipation	P _C	200	mW				
Junction temperature	Tj	150	°C				
Operating ambient temperature	T _{opr}	-40 to +85	°C				
Storage temperature	T _{stg}	-55 to +150	°C				



Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \ \mu {\rm A}, I_{\rm E} = 0$	60			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 2 {\rm mA}, I_{\rm B} = 0$	50			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = 10 \ \mu {\rm A}, I_{\rm C} = 0$	5			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 20 \text{ V}, I_E = 0$			0.1	μΑ
Forward current transfer ratio *1	h _{FE1} *2	$V_{CE} = 10 \text{ V}, I_C = 150 \text{ mA}$	120		340	
	h _{FE2}	$V_{CE} = 10 \text{ V}, I_C = 500 \text{ mA}$	40			
Collector-emitter saturation voltage *1	V _{CE(sat)}	$I_{\rm C} = 300 \text{ mA}, I_{\rm B} = 30 \text{ mA}$		0.1	0.6	V
Transition frequency	f _T	$V_{CE} = 10 \text{ V}, I_C = 50 \text{ mA}$		160		MHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		4.8	15	pF

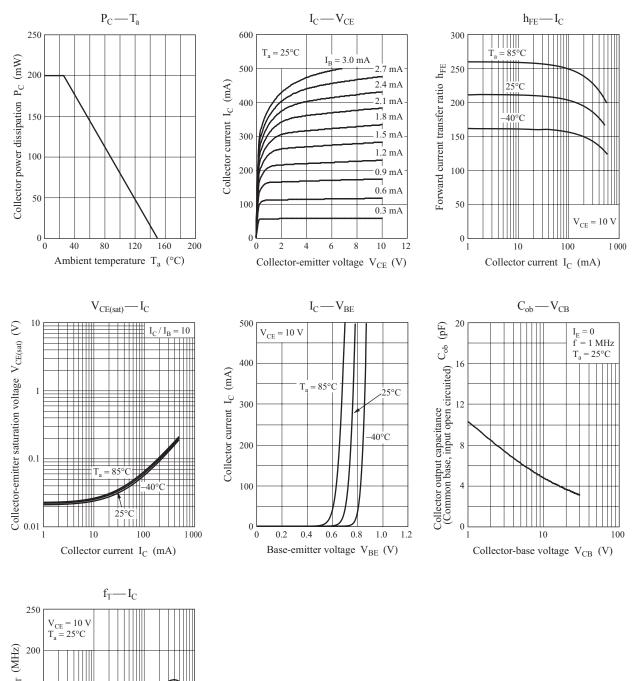
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

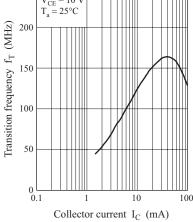
2. *1: Pulse measurement

*2: Rank classification

Code	R	S	0			
Rank	R	S	No-rank			
$h_{\rm FE}$	120 to 240	170 to 340	120 to 340			
Marking Symbol	C2R	C2S	C2			

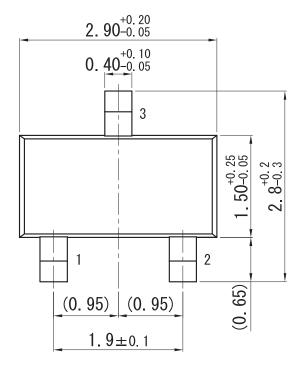
Product of no-rank is not classified and have no marking symbol for rank.

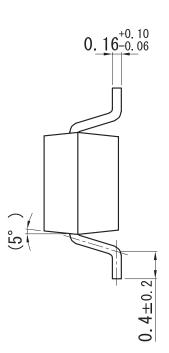


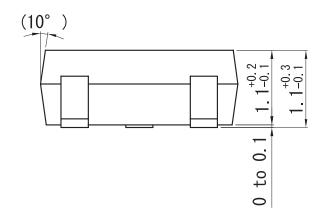


Unit: mm

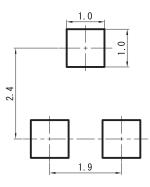
Mini3-G3-B-B







Land Pattern (Reference) (Unit: mm)



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