# **DSC5C01**

## Silicon NPN epitaxial planar type

For low frequency amplification DSC2C01 in SMini3 type package

#### ■ Features

- High forward current transfer ratio h<sub>FE</sub> with excellent linearity
- $\bullet$  Low collector-emitter saturation voltage  $V_{\text{CE}(\text{sat})}$
- Halogen-free / RoHS compliant
   (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)

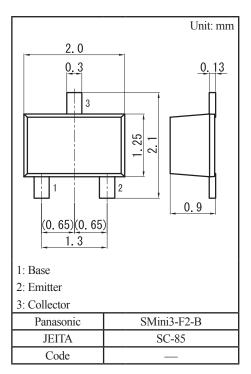
#### ■ Marking Symbol: C9

#### Packaging

DSC5C01×0L Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

## ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	100	V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	100	V
Emitter-base voltage (Collector open)	$V_{\mathrm{EBO}}$	15	V
Collector current	$I_{C}$	20	mA
Peak collector current	I <sub>CP</sub>	50	mA
Collector power dissipation	P <sub>C</sub>	150	mW
Junction temperature	Tj	150	°C
Operating ambient temperature	T <sub>opr</sub>	-40 to +85	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C



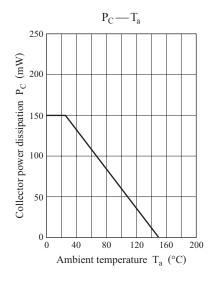
### ■ Electrical Characteristics $T_a = 25$ °C±3°C

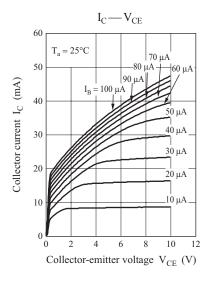
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	$I_C = 10 \mu A, I_E = 0$	100			V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$	100			V
Emitter-base voltage (Collector open)	V <sub>EBO</sub>	$I_E = 10 \mu A, I_C = 0$	15			V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{CB} = 60 \text{ V}, I_{E} = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I <sub>CEO</sub>	$V_{CE} = 60 \text{ V}, I_{B} = 0$			1	μΑ
Forward current transfer ratio *1	h <sub>FE</sub>	$V_{CE} = 10 \text{ V}, I_{C} = 2 \text{ mA}$	400		1200	_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 1 \text{ mA}$		0.05	0.20	V
Transition frequency	$f_T$	$V_{CE} = 10 \text{ V}, I_{C} = 2 \text{ mA}$		140		MHz

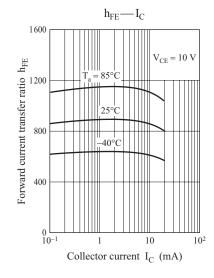
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

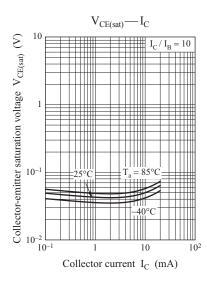
#### 2. \*1: Rank classification

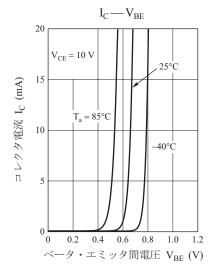
Code	R	S
Rank	R	S
$h_{\mathrm{FE}}$	400 to 800	600 to 1200
Marking Symbol	C9R	C9S

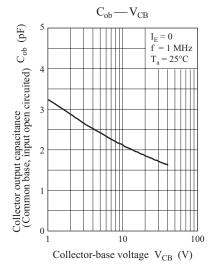


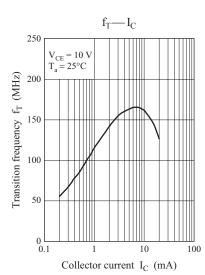








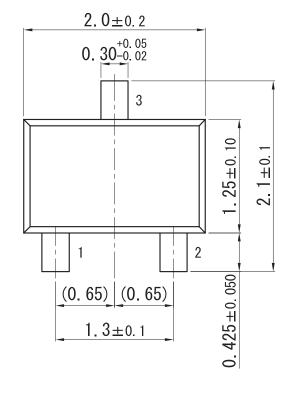


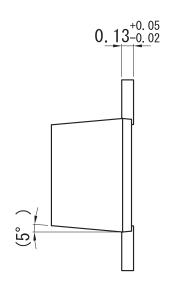


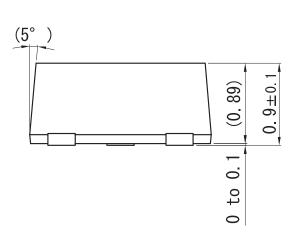
Ver. CED 2

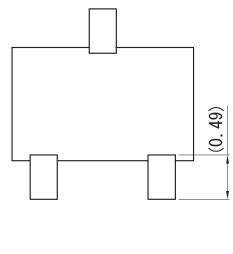
## SMini3-F2-B

Unit: mm

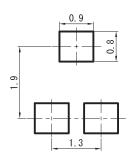








### ■ Land Pattern (Reference) (Unit: mm)



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