MOS FET

SK8403180L

Panasonic

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Silicon N-channel MOS FET

For Load-switching / For DC-DC Converter

■ Features

- Low Drain-source On-state Resistance : RDS(on) typ = 6.7 m Ω (VGS = 4.5 V)
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL : Level 1 compliant)

■ Marking Symbol : 18

Established: 2013-01-07

: 2013-05-31

Revised

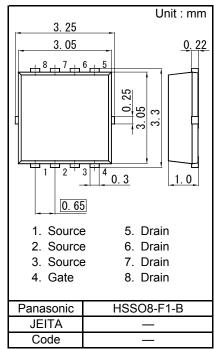
■ Packaging

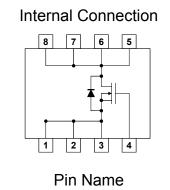
Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

■ Absolute Maximum Ratings Ta = 25 °C

Parameter			Symbol	Rating	Unit		
Drain to Source Voltage			VDS	30	V		
Gate to Source Voltage			VGS	±20			
Drain Current	Ta = 25 °C, t = 10 s *1		ID	17			
	Ta = 25 °C, DC *1			12	^		
	Tc = 25 °C			39	Α		
	Pulsed	d, Tch < 150 °C ^{*2}		51			
Total Power			PD	2	W		
Dissipation		Ta = 25 °C, DC *1 Tc = 25 °C	PD	19			
Thermal Resistance		Channel to Ambient	Rth(ch-a)	62.5	°C / W		
memai Kesisi	ance	Channel to Case	Rth(ch-c)	6.6	C / VV		
Channel Temperature			Tch	150			
Operating ambient temperature			Topr	-40 to +85	°C		
Storage Temperature Range			Tstg	-55 to +150			
Avalanche Current (Single pulse) *3		IAR	8.5	Α			
Avalanche Energy (Single pulse) *3			EAR	9	mJ		

- Note *1 Device mounted on a glass-epoxy board in Figure 1
 - *2 Pulse test: Ensure that the channel temperature does not exceed 150 °C
 - *3 VDD = 24 V, VGS = 10 to 0 V, L = 0.1 mH, Tch = 25 $^{\circ}$ C (initial)





- Source
 Source
 Drain
 Drain
- 3. Source 7. Drain 4. Gate 8. Drain
 - . Gate 6. Diam



Figure 1 FR4 Glass-Epoxy Board 25.4 mm × 25.4 mm × 0.8 mm

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■ Electrical Characteristics Ta = 25 °C ± 3 °C

Static Characteristics

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-source Breakdown Voltage	VDSS	ID = 1 mA, VGS = 0 V	30			V
Zero Gate Voltage Drain Current	IDSS	VDS = 30 V, VGS = 0 V			10	μΑ
Gate-source Leakage Current	IGSS	VGS = ± 16 V, VDS = 0 V			±10	μΑ
Gate-source Threshold Voltage		ID = 1.45 mA, VDS = 10 V	1.3		3	V
Drain-source On-state Resistance		ID = 8.5 A, VGS = 10 V		5.1	7.1	mΩ
Diani-source On-sidle Nesistance	RDS(on)2	ID = 8.5 A, VGS = 4.5 V		6.7	9.8	

Dynamic Characteristics

Parameter	Symbol	Conditions	Min	Tyn	Max	Unit
		Conditions	IVIIII	Тур		Offic
Input Capacitance	Ciss	VDS = 10 V, VGS = 0 V		1 200	1 680	
Output Capacitance	Coss	f = 1 MHz		140	200	pF
Reverse Transfer Capacitance	Crss	=		100	160	
Turn-on Delay Time *1	td(on)	VDD = 15 V, VGS = 0 to 10 V		8		no
Rise Time *1	tr	ID = 8.5 A		6		ns
Turn-off Delay Time *1	td(off)	VDD = 15 V, VGS = 10 to 0 V		39		no
Fall Time *1	tf	ID = 8.5 A		6		ns
Total Gate Charge	Qg	VDD = 15 V VCS = 0 to 4 5 V		10		
Gate to Source Charge	Qgs	VDD = 15 V, VGS = 0 to 4.5 V ID = 8.5 A		3		nC
Gate to Drain Charge	Qgd	U - 0.5 A		4		
Gate resistance	rg	f = 5 MHz		1.2	3	Ω

Body Diode Characteristic

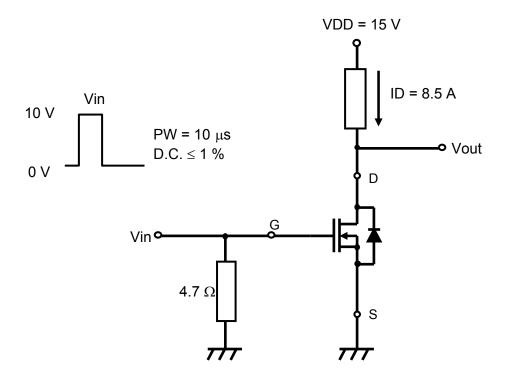
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Diode Forward Voltage	VSD	IS = 8.5 A, VGS = 0 V		0.8	1.2	V

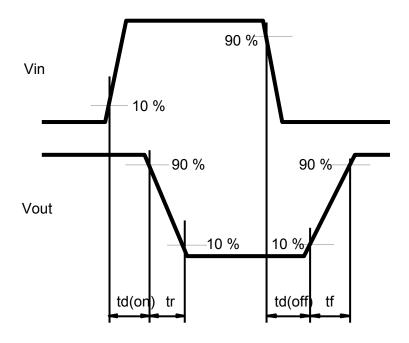
Note: 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

^{2. *1} Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time

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*1 Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time

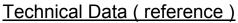


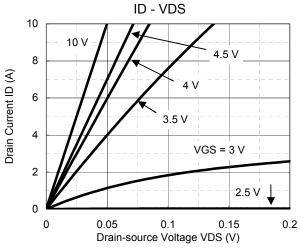


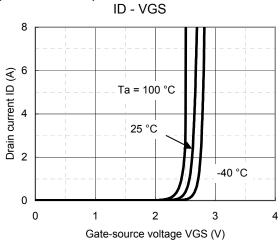
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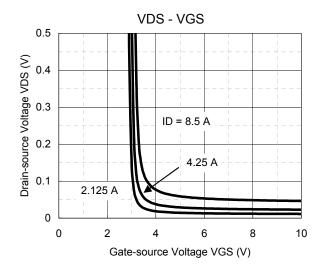
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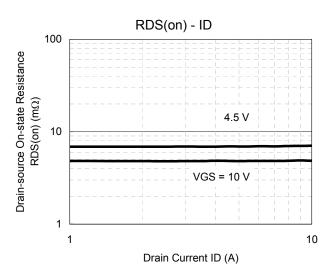
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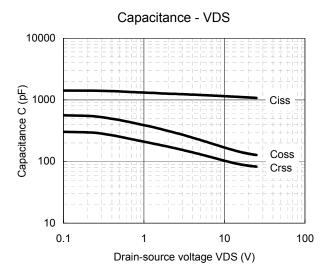


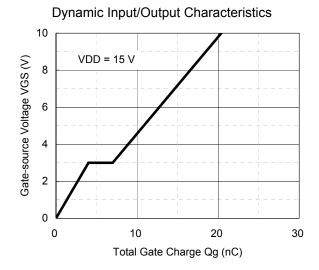








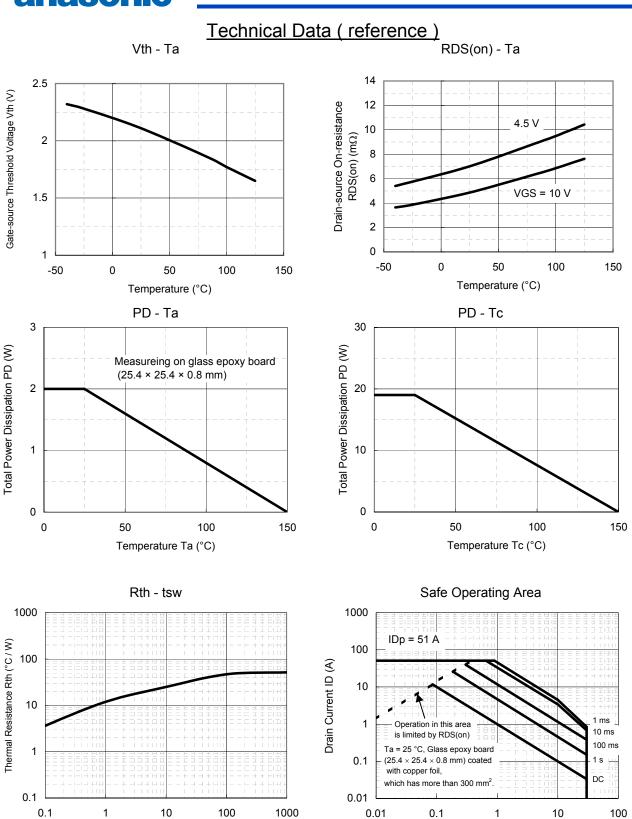




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Drain-source Voltage VDS (V)

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Pulse Width tsw (s)

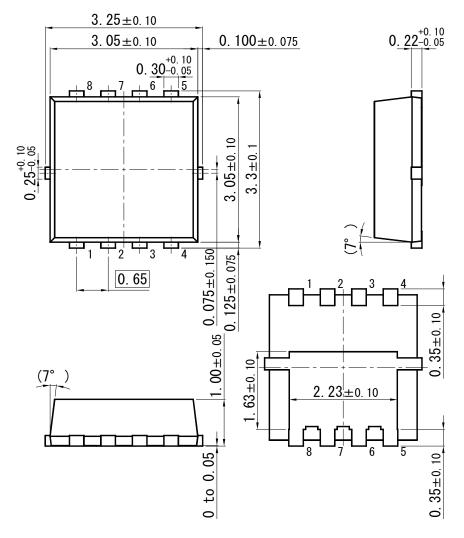
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HSSO8-F1-B

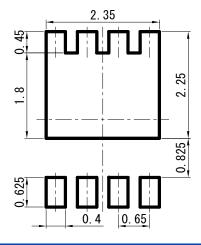
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Unit: mm



■ Land Pattern (Reference) (Unit : mm)

Established: 2013-01-07 Revised: 2013-05-31



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