

| | |
|-----------------------|------|
| V_{CES} | 650V |
| $I_{C(100^{\circ}C)}$ | 25A |
| $V_{CE(sat)}$ (Typ.) | 1.6V |
| P_D | 174W |

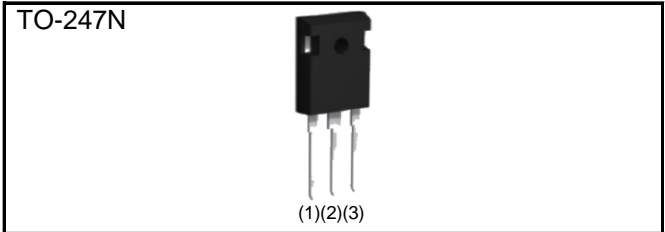
●Features

- 1) Low Collector - Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Pb - free Lead Plating ; RoHS Compliant

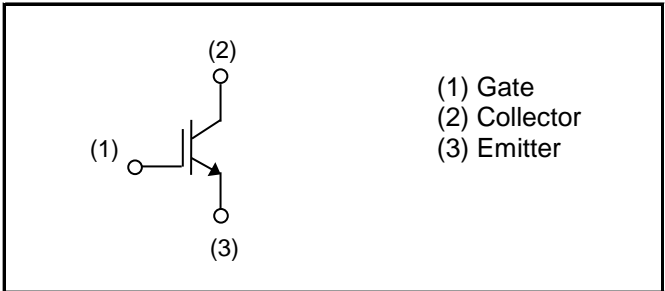
●Applications

PFC
 UPS
 Power Conditioner
 IH

●Outline



●Inner Circuit



●Packaging Specifications

| | | |
|------|---------------------------|------------|
| Type | Packaging | Tube |
| | Reel Size (mm) | - |
| | Tape Width (mm) | - |
| | Basic Ordering Unit (pcs) | 450 |
| | Taping Code | C11 |
| | Marking | RGTH50TS65 |

●Absolute Maximum Ratings (at $T_C = 25^{\circ}C$ unless otherwise specified)

| Parameter | Symbol | Value | Unit | |
|--------------------------------|----------------------|-------------|-------------|---|
| Collector - Emitter Voltage | V_{CES} | 650 | V | |
| Gate - Emitter Voltage | V_{GES} | ± 30 | V | |
| Collector Current | $T_C = 25^{\circ}C$ | I_C | 50 | A |
| | $T_C = 100^{\circ}C$ | I_C | 25 | A |
| Pulsed Collector Current | I_{CP}^{*1} | 100 | A | |
| Power Dissipation | $T_C = 25^{\circ}C$ | P_D | 174 | W |
| | $T_C = 100^{\circ}C$ | P_D | 87 | W |
| Operating Junction Temperature | T_j | -40 to +175 | $^{\circ}C$ | |
| Storage Temperature | T_{stg} | -55 to +175 | $^{\circ}C$ | |

*1 Pulse width limited by T_{jmax} .

● Thermal Resistance

| Parameter | Symbol | Values | | | Unit |
|---|-------------------|--------|------|------|------|
| | | Min. | Typ. | Max. | |
| Thermal Resistance IGBT Junction - Case | $R_{\theta(j-c)}$ | - | - | 0.86 | °C/W |

● IGBT Electrical Characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Conditions | Values | | | Unit |
|--|---------------|--|--------|------|-----------|---------------|
| | | | Min. | Typ. | Max. | |
| Collector - Emitter Breakdown Voltage | BV_{CES} | $I_C = 10\mu\text{A}$, $V_{GE} = 0\text{V}$ | 650 | - | - | V |
| Collector Cut - off Current | I_{CES} | $V_{CE} = 650\text{V}$, $V_{GE} = 0\text{V}$ | - | - | 10 | μA |
| Gate - Emitter Leakage Current | I_{GES} | $V_{GE} = \pm 30\text{V}$, $V_{CE} = 0\text{V}$ | - | - | ± 200 | nA |
| Gate - Emitter Threshold Voltage | $V_{GE(th)}$ | $V_{CE} = 5\text{V}$, $I_C = 17.5\text{mA}$ | 4.5 | 5.5 | 6.5 | V |
| Collector - Emitter Saturation Voltage | $V_{CE(sat)}$ | $I_C = 25\text{A}$, $V_{GE} = 15\text{V}$ | - | 1.6 | 2.1 | V |
| | | $T_j = 25^\circ\text{C}$ | - | 2.1 | - | |
| | | $T_j = 175^\circ\text{C}$ | - | | | |

●IGBT Electrical Characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Conditions | Values | | | Unit |
|----------------------------------|--------------|--|-------------|------|------|------|
| | | | Min. | Typ. | Max. | |
| Input Capacitance | C_{ies} | $V_{CE} = 30\text{V}$ | - | 1410 | - | pF |
| Output Capacitance | C_{oes} | $V_{GE} = 0\text{V}$ | - | 57 | - | |
| Reverse Transfer Capacitance | C_{res} | $f = 1\text{MHz}$ | - | 22 | - | |
| Total Gate Charge | Q_g | $V_{CE} = 300\text{V}$ | - | 49 | - | nC |
| Gate - Emitter Charge | Q_{ge} | $I_C = 25\text{A}$ | - | 15 | - | |
| Gate - Collector Charge | Q_{gc} | $V_{GE} = 15\text{V}$ | - | 19 | - | |
| Turn - on Delay Time | $t_{d(on)}$ | $I_C = 25\text{A}, V_{CC} = 400\text{V}$ | - | 27 | - | ns |
| Rise Time | t_r | $V_{GE} = 15\text{V}, R_G = 10\Omega$ | - | 38 | - | |
| Turn - off Delay Time | $t_{d(off)}$ | $T_j = 25^\circ\text{C}$ | - | 94 | - | |
| Fall Time | t_f | Inductive Load | - | 50 | - | |
| Turn - on Delay Time | $t_{d(on)}$ | $I_C = 25\text{A}, V_{CC} = 400\text{V}$ | - | 27 | - | ns |
| Rise Time | t_r | $V_{GE} = 15\text{V}, R_G = 10\Omega$ | - | 38 | - | |
| Turn - off Delay Time | $t_{d(off)}$ | $T_j = 175^\circ\text{C}$ | - | 107 | - | |
| Fall Time | t_f | Inductive Load | - | 65 | - | |
| Reverse Bias Safe Operating Area | RBSOA | $I_C = 100\text{A}, V_{CC} = 520\text{V}$ $V_P = 650\text{V}, V_{GE} = 15\text{V}$ $R_G = 60\Omega, T_j = 175^\circ\text{C}$ | FULL SQUARE | | | - |

●Electrical Characteristic Curves

Fig.1 Power Dissipation vs. Case Temperature

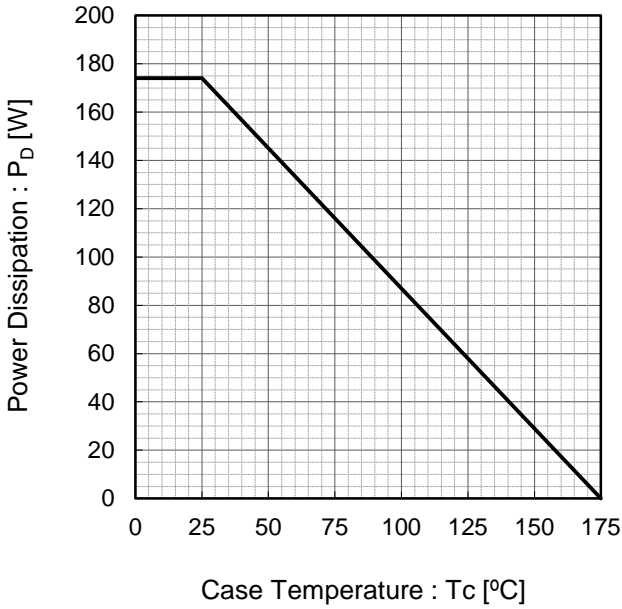


Fig.2 Collector Current vs. Case Temperature

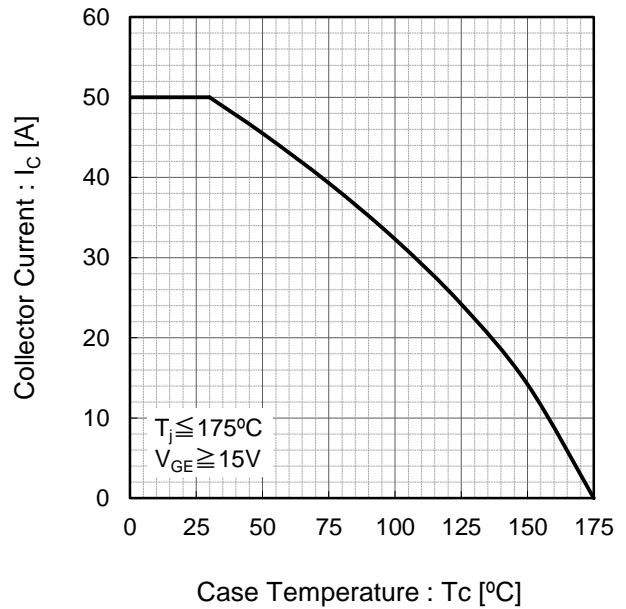


Fig.3 Forward Bias Safe Operating Area

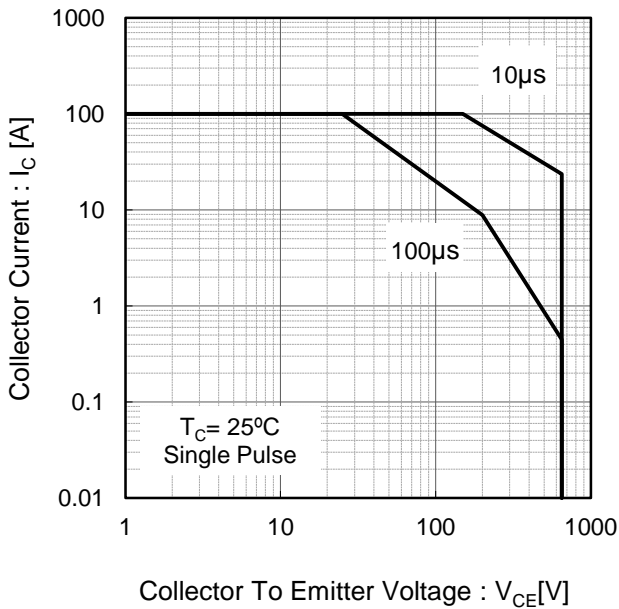
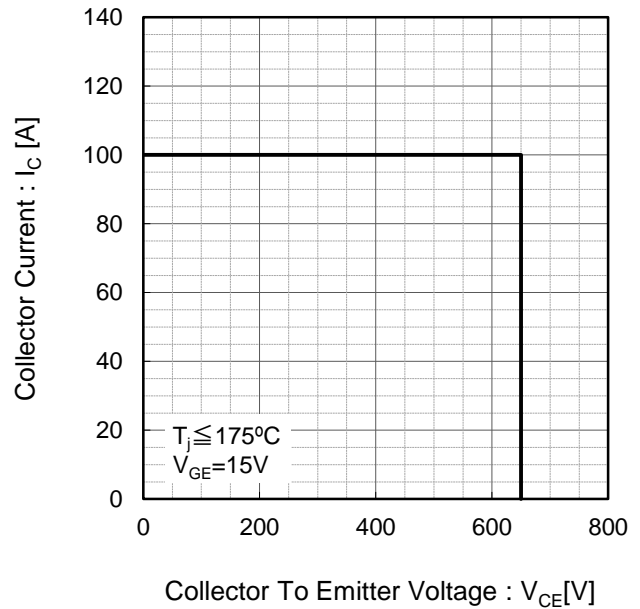


Fig.4 Reverse Bias Safe Operating Area



●Electrical Characteristic Curves

Fig.5 Typical Output Characteristics

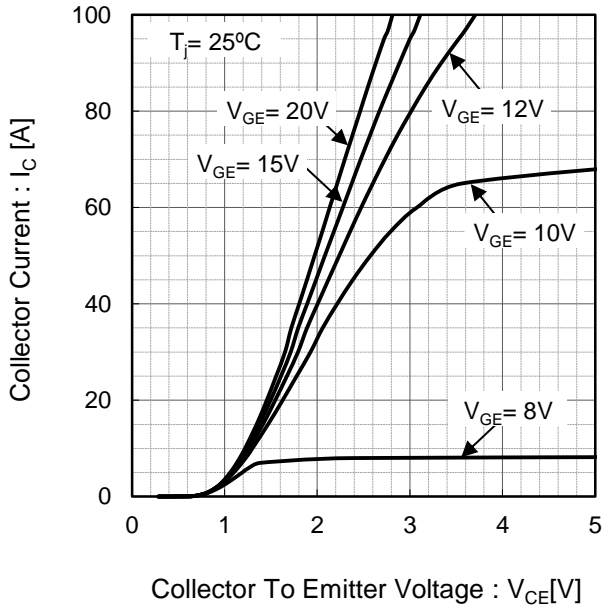


Fig.6 Typical Output Characteristics

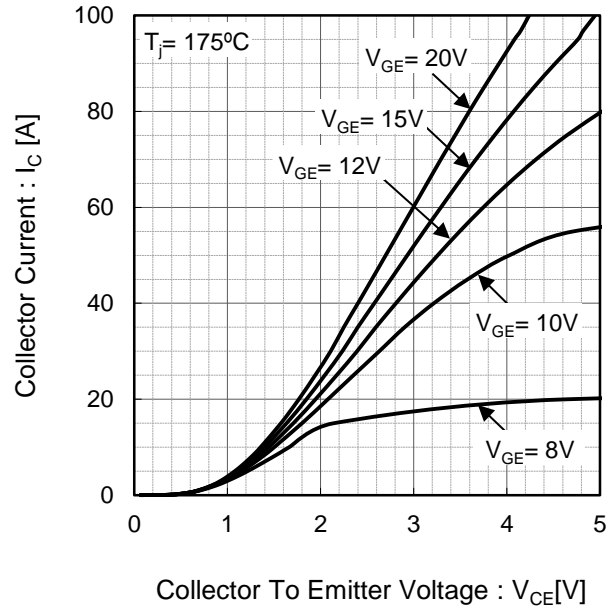


Fig.7 Typical Transfer Characteristics

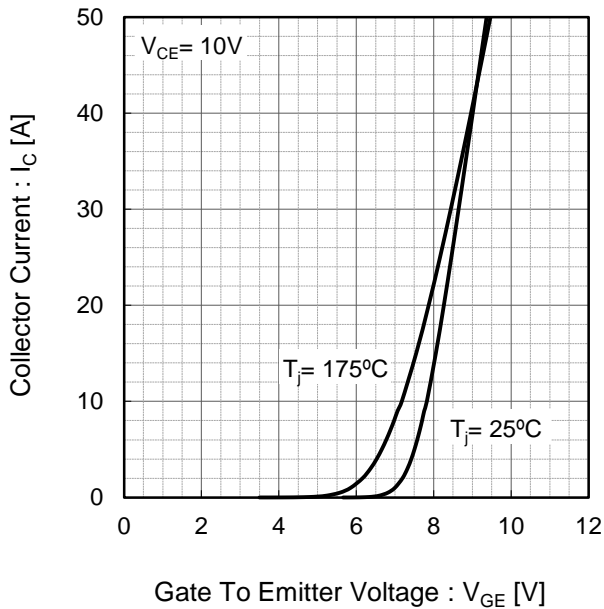
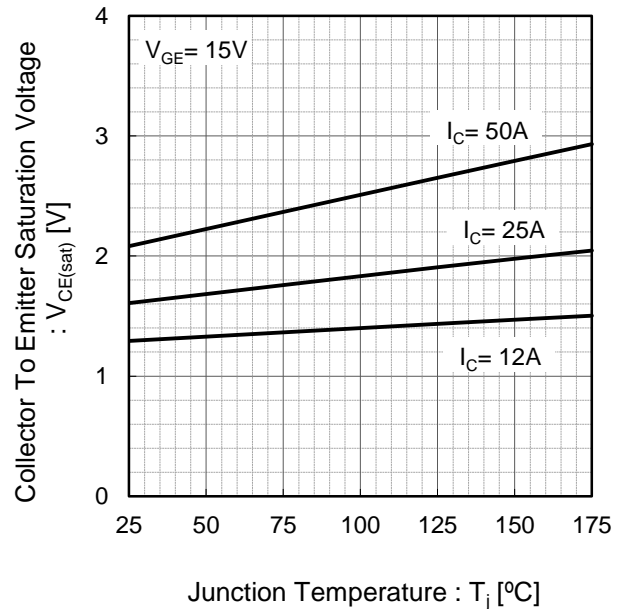


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



●Electrical Characteristic Curves

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

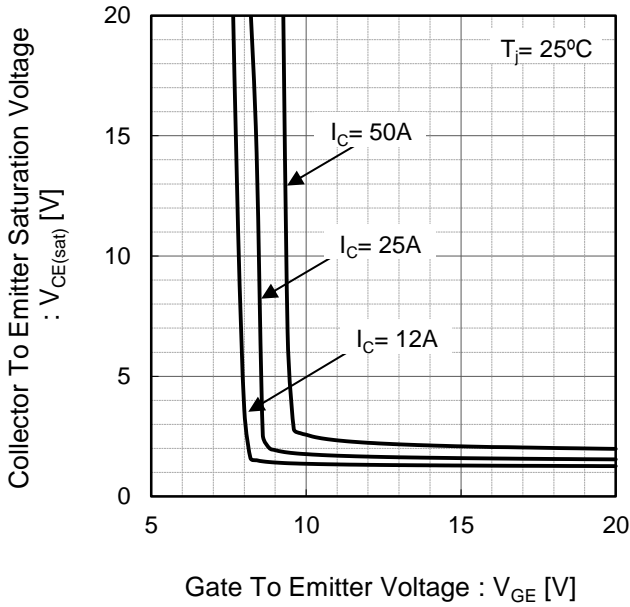


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

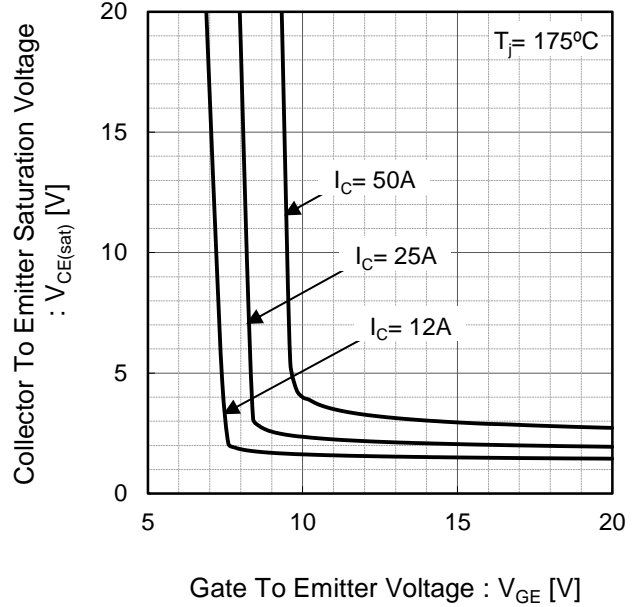


Fig.11 Typical Switching Time vs. Collector Current

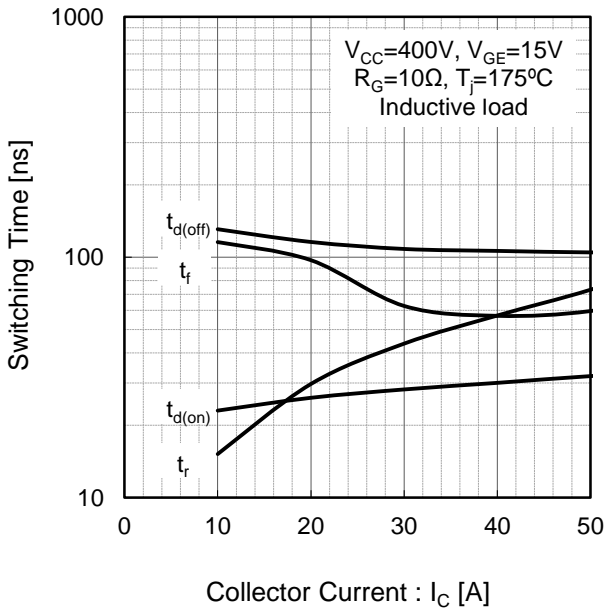
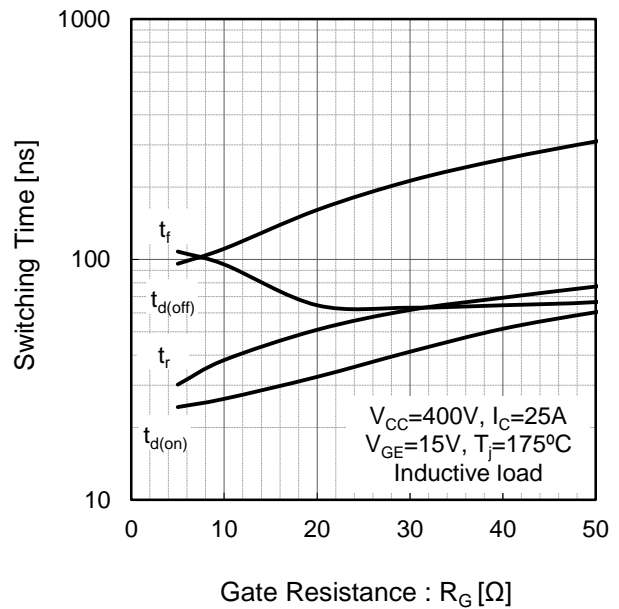


Fig.12 Typical Switching Time vs. Gate Resistance



●Electrical Characteristic Curves

Fig.13 Typical Switching Energy Losses vs. Collector Current

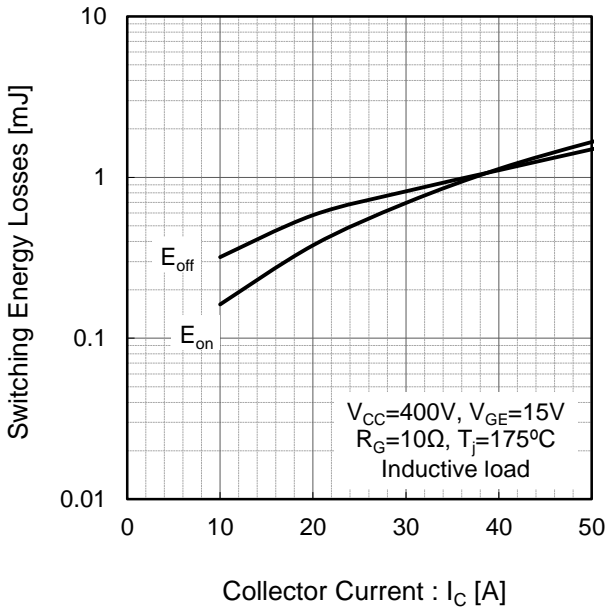


Fig.14 Typical Switching Energy Losses vs. Gate Resistance

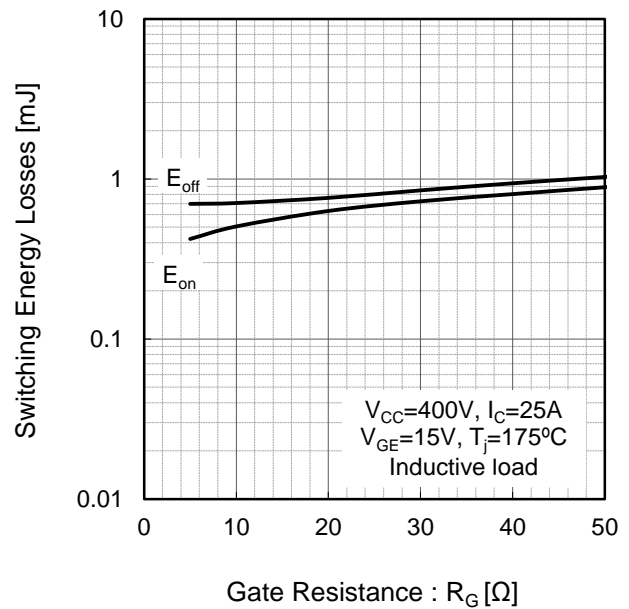


Fig.15 Typical Capacitance vs. Collector To Emitter Voltage

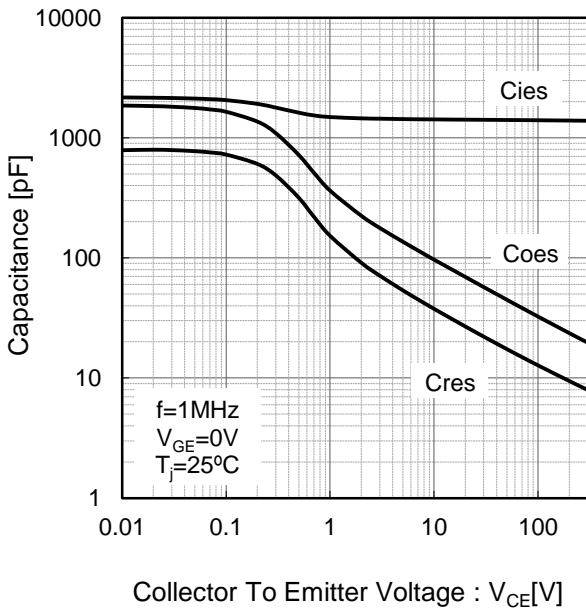
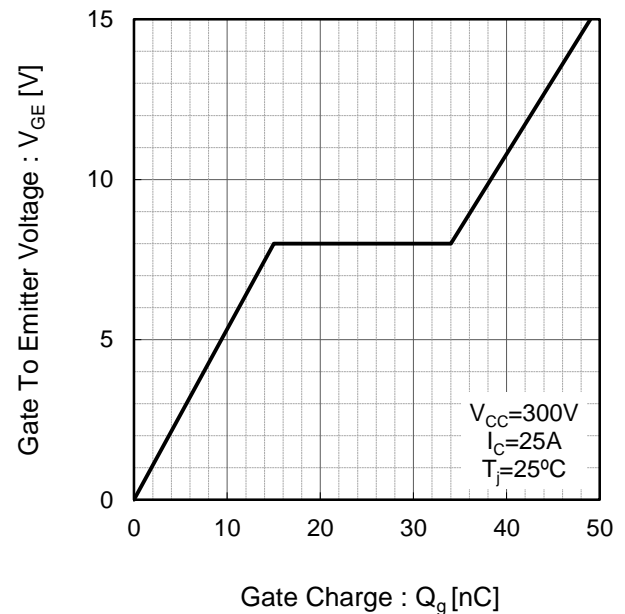
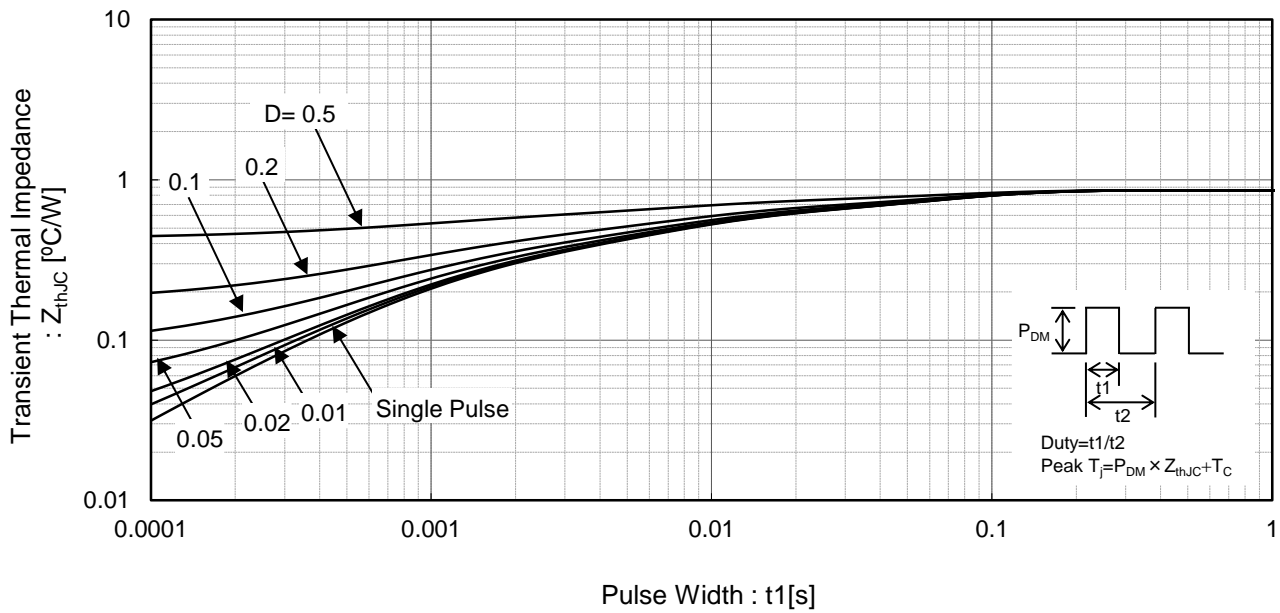


Fig.16 Typical Gate Charge



●Electrical Characteristic Curves

Fig.17 IGBT Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

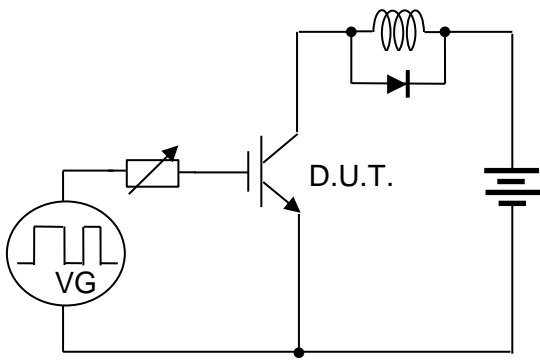


Fig.18 Inductive Load Circuit

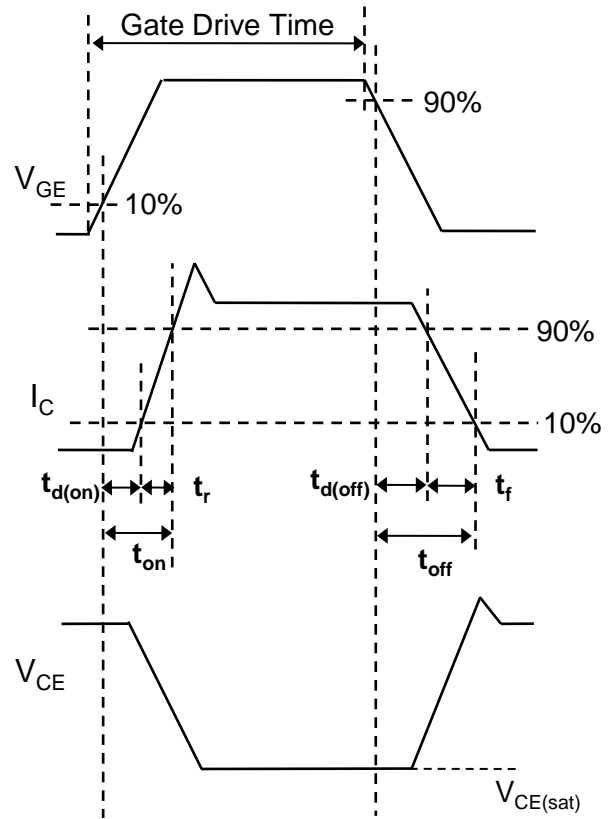


Fig.19 Inductive Load Waveform

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